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MICRO JOURNAL

VOLUME V ISSUE VI • Devoted to the 68XX User • June 1983
"Small Computers Doing Big Things"

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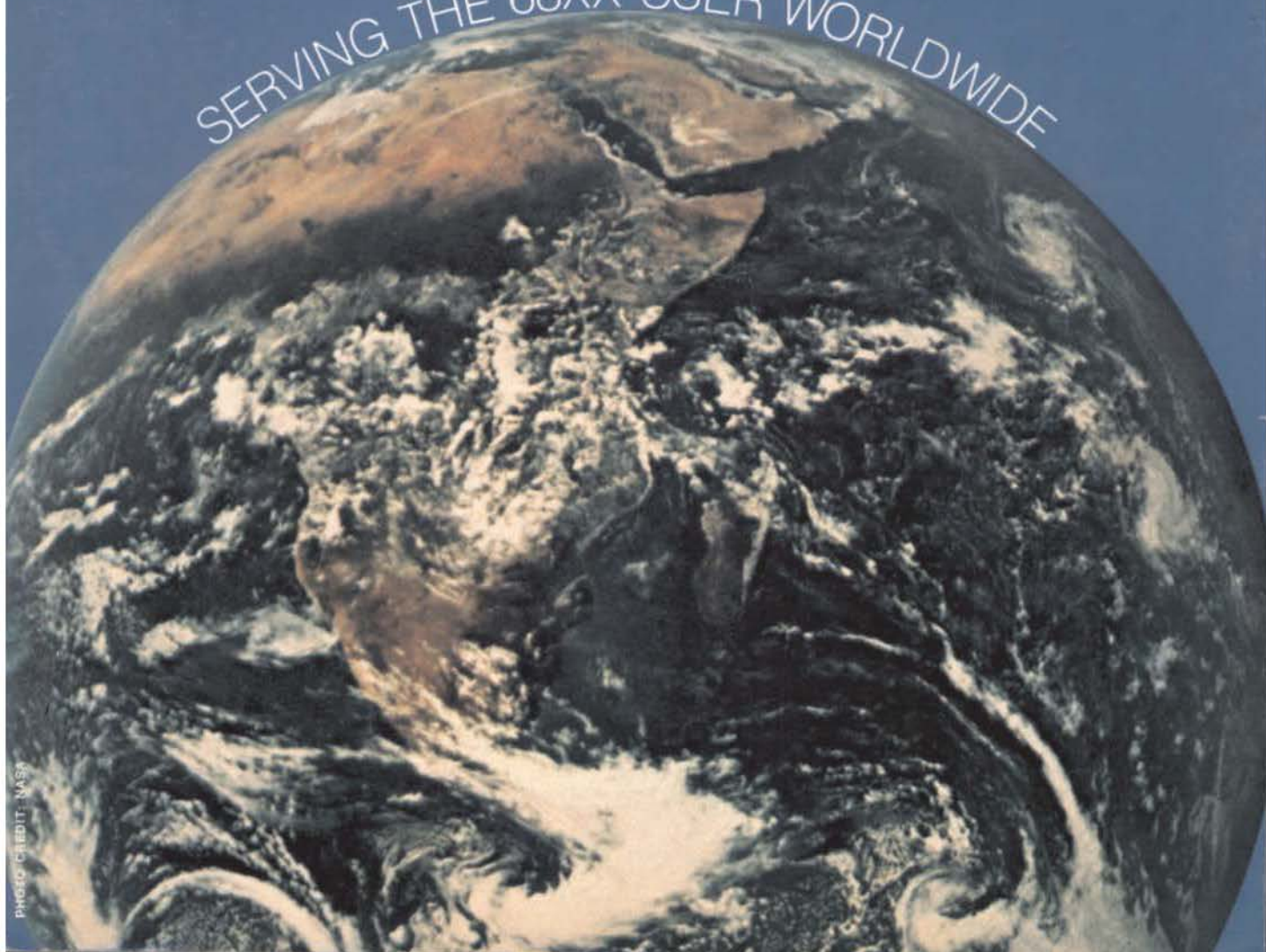
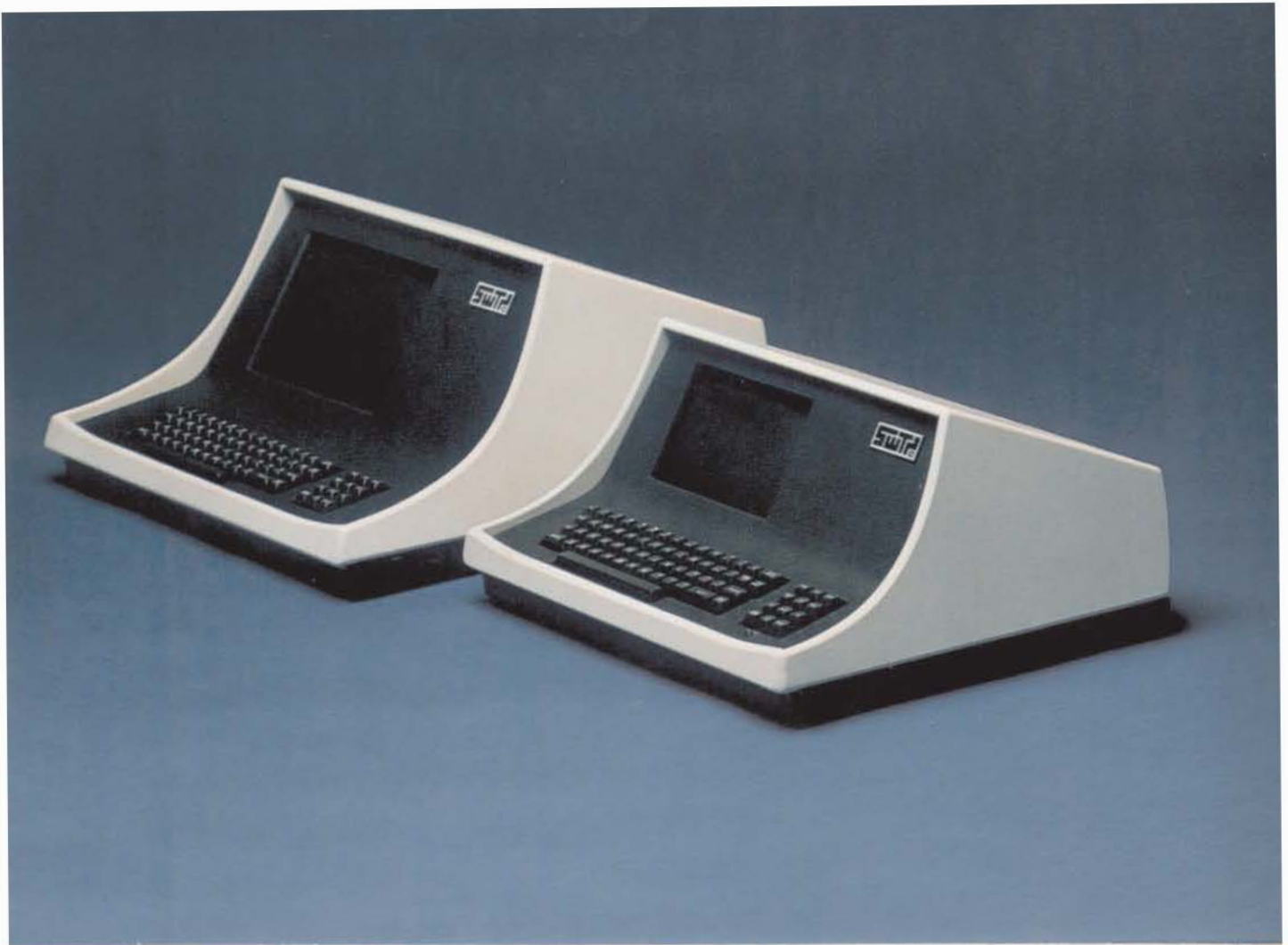


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FOREIGN

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Items Submitted for Publication

Articles submitted for publication should be accompanied by the authors full name, address, date and telephone number. It is preferred that articles be submitted on either 5 or 8 inch diskette in TSC Editor format or STYLO format. All diskettes will be returned.

The following TSC Text Processor commands ONLY should be used (due to our proportional processor): .sp space, .pp paragraph, .fi fill and .nf no fill. Also please do not format within the text with multiple spaces. The rest we will enter at time of editing.

STYLO commands are all acceptable except the .pg page command, we print edited text files in continuous text.

All articles submitted on diskettes should be in TSC FLEX* format, either FLEX2 6800, or FLEX9 6809 any version.

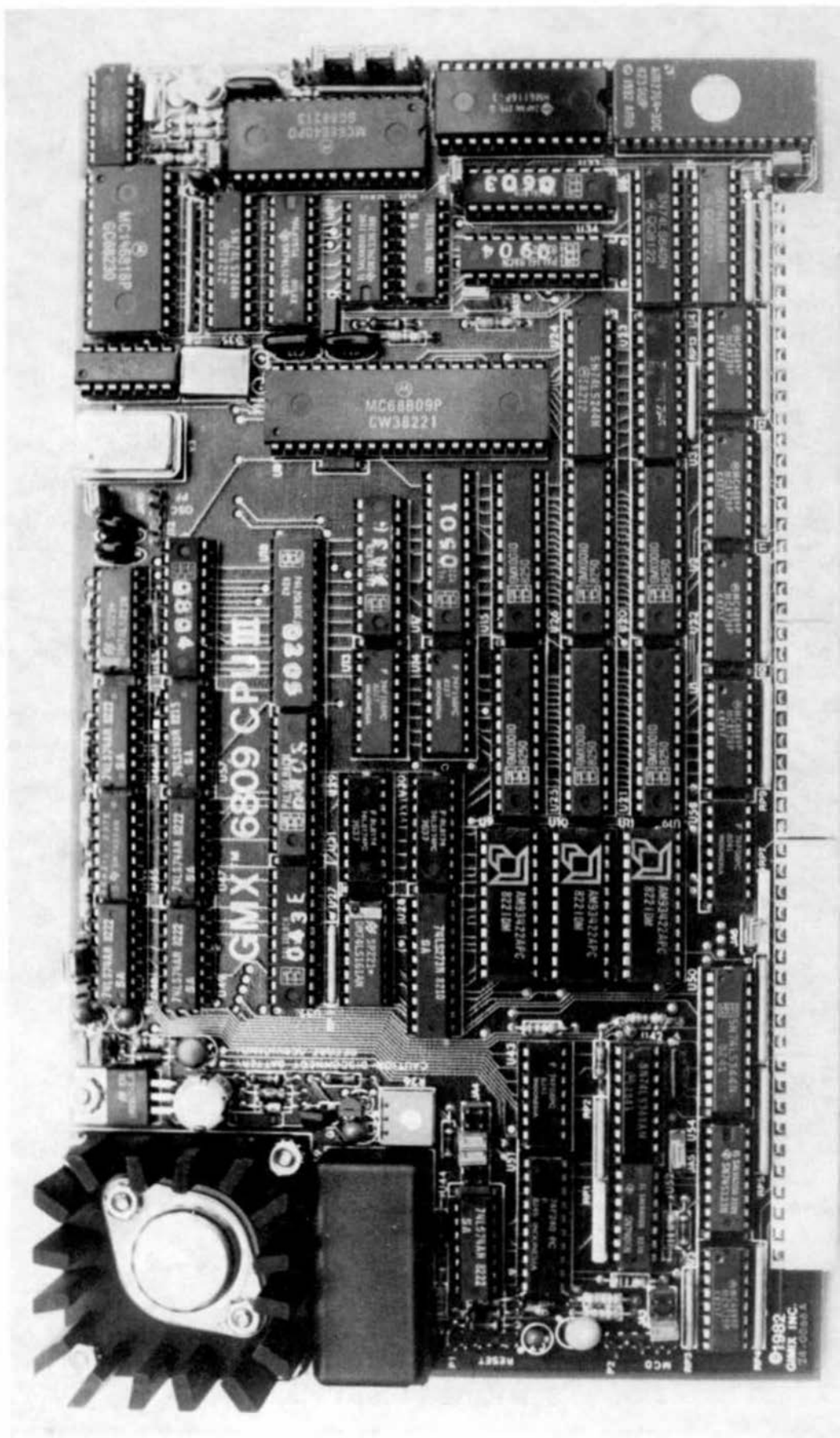
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All letters to the editor should also comply with the above and bear a signature. Letters of 'gripes' as well as 'praise' are solicited. We attempt to publish all letters to the editor verbatim, however, we reserve the right to reject any submission for lack of 'good taste'. We reserve the right to define what constitutes 'good taste'.

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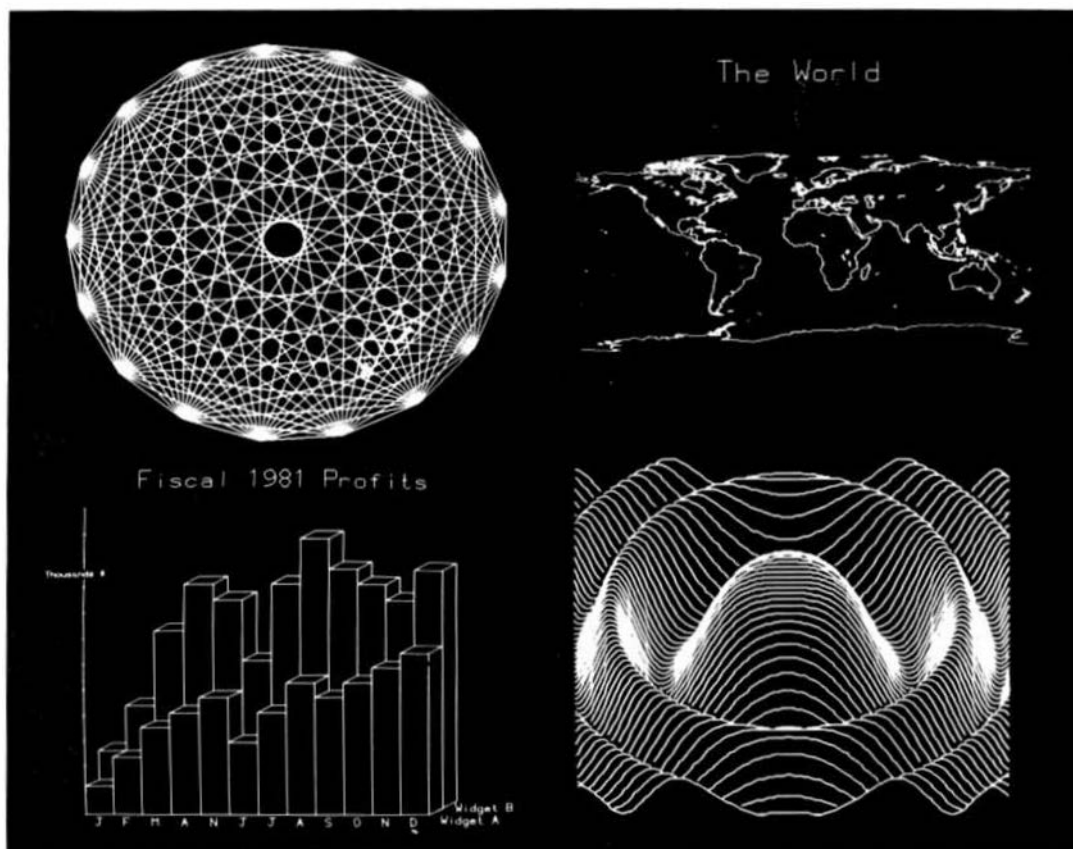
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Single character commands for simplicity.

Hardware independent (No interrupts required). Assumes modem is connected to an MC6850 (serial interface) and the control terminal is connected to an MC6850 (or MC6820 when used with the video version of GIMBUG™).

Transmit manually to distant computer

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Receive and save disk files (text) of any length on local disk system. If sending computer does not support an X-on/X-off protocol, then the received files are limited in size by the computer memory.

Tested for full duplex operation at speeds up to 9600 baud. (CRT terminal must be capable of operating at a baud rate higher than the one the modem is operated at.)

Half duplex option in case distant computer doesn't echo

Echo option so user can simulate a time sharing system. (Super Modem Program doesn't support auto-answer but the source is provided for those individuals who wish to adapt our program to their special needs.)

Replaces CR with CR/LF (user option) for those using time sharing systems that don't transmit LF's

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Editor - Text Processor - Mailing Labels
Mailing Lists - Use any CRT terminal and printer

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Supports Text Processing commands such as block copy, block move, centering, margin justification (widen and narrow), paging, and tabbing.

Mailing Lists and Labels. Use the same mailing list disk file (with protected areas) for both mailing labels and repeat letters. Repeat letters are personally addressed to each person - selected persons on the mailing list.

Most Powerful File Handler found in any editor. Append one file to the end of another, or insert (merge) one file into another as designated by the line pointer. Print specified lines to your printer or to a disk file. Edit files larger than the text buffer. Does not produce output files when not desired. Delete disk files from the editor.

Printer commands. control characters can be sent to the printer for format control either directly from the control terminal or by imbedding them in the text. The set command contains interface initialization and character output routines to support the SWTPC MP-C interface as well as the standard serial and parallel interfaces. Jumps are also provided to user supplied printer routines. User selects the port address (0 thru 7, A or B) thereby eliminating the need for the user to install printer software routines. Editor can be initialized for either 4 or 16 addresses per port.

Editor allows exiting to either the monitor or DOS and then reenter (Warm start) without destroying previously prepared text in the buffer. The Restart command erases contents in the buffer without the user having to reload the Editor.

The Editor allows the user to toggle between full duplex (no echo) and half duplex (echo) as needed. It responds to commands in both upper and lower case and can be used to create assembler source code and Basic programs as well as text.

Specify 6800 or 6809, SSB or FLEX™, 5" or 8"
Printed source listing is available for an additional
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OS-9™ Interactive Assembler		300.00	10.00	125.00	
OS-9™ Interactive Debugger (Disk version)		100.00	10.00	50.00	
CIS Cobol Compiler	400.00	50.00	N/A	80.00	800.00
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* SSB BFD Floppy Disk Controllers (Version 3)	175.00
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Flex User Notes

Ronald W. Anderson
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Ann Arbor, MI 48105

Heating Control

Last time, we had started the discussion of a home heating control to illustrate the use of a 6809 system to perform a control function. We had gotten as far as a program to use the JPC A/D board to read temperatures and temperature commands on up to 8 rooms (16 channels) and display the readings and commands on the terminal. We also showed the schematic for the hardware required for that portion of the control.

In order to keep things fairly simple, I am going to opt for a bit more hardware on the output side than is absolutely necessary, in order to keep the software simpler. If you follow what is presented, you can undoubtedly adapt the idea to whatever sort of actuator and sensors you have available.

Safety (or failsafe operation) must be considered as a primary feature. The original thermostat will be left connected. You may set it to a temperature lower than you will want in the house while you are present. If the house power falls, of course the gas valve can't be turned on while the power is off. However, the return of power must not cause the system to call for heat. Unfortunately, the 6821 PIA can supply no source current when it is used as an output, so a pull up resistor is required. When it is reset, it comes up as an input, and the pull up will cause the output to go high, making it look as though it were on. We must therefore provide a "latching relay" that will drop out on power failure and disable the system until it is reset manually after power returns.

Such a feature won't be necessary if you plan on using a single board computer and have the program in ROM, because power restart will start the program and reinitialize the PIA used in the output, as well as the one on the A/D board. However, if you use a system in which the program is loaded to RAM from a disk or tape, the manual reset is absolutely necessary. The thermostat will take over and prevent the pipes from freezing, etc.

Probably the best bet when the system is run from RAM is to resort to the manual thermostat when you are not home, anyway. I've spent time and words on this problem because it is typical of a machine control application. When the power falls or is shut off by an "Emergency Stop", the control must return to a "benign" state so that the reapplication of power leaves it in its "Manual" mode, and with no spindles or part transfer mechanisms in motion. An error on the part of the programmer in that regard could cost a maintenance technician a finger, hand, arm, or worse, his life.

Now, on to our scheme. We will use one port of a PIA to provide 8 outputs. These outputs will signal "Baffle Open" or "Water Valve Open" when high, and the reverse when low. Such an arrangement is quite simple to interface with a solenoid valve, since all that is needed is a power amplifier or a relay for each valve. In the case of a motorized baffle in a heat duct, the control means for reversing the motor, and sensing its travel limits with two limit switches, must be supplied. See the diagram for the control arrangement. Most heat control is done with 24 volts AC which is not dangerous to work with. Some units use 120 volts, however, which is VERY hazardous. In any case it is good practice to turn off the control power when working on the system.

The software must not call on the furnace to turn on and off at too frequent intervals. We must therefore provide for several possibilities. It seems to me that a

good way to minimize cycling is to provide for a couple of degrees "hysteresis" in the control. That is, if the temperature is set for 70 degrees in a room, the control will supply heat until the temperature reaches 71, and will not again call for heat until the temperature reaches 69. Depending on your installation, you might want to widen those limits a bit more. It might also be prudent to require at least two zones calling for heat before turning on the furnace. A system set to supply air sufficient for ducts to 8 rooms, might overheat if all but one duct were to be shut. An alternative to this is to set the "closed" limit on the baffles such that all are open to about 20% of their wide open air flow.

Furnace controls have overtemperature sensors that turn the flame off but leave the air on. If you install this system, you should check the overtemperature limit to see that it is not being reached when only two ducts are open. If that is a problem, you might want to require three zones calling for heat before turning on the furnace. Control will still be vastly better than that obtained with a single thermostat in the living room.

I've added the necessary software to put the readings in an array and then perform the necessary logic to open or close the various zone ducts or valves. The additions include a section in the initialize routine to set up the PIA properly, and the logic to open ducts or valves and call for heat. The heat contact should be wired in parallel with the normally open contacts of your house heating thermostat. Note that I've used both ports of the PIA as outputs. MP-LA cards will require jumpers to select output buffering on both sides of the port, and the old MP-L cards will require foil cuts and jumpers to do the same thing. The newest systems have programmable buffer data direction registers included in them. See your parallel port instruction manual for the necessary programming instructions, and include them in the initialize routine. I realize that it would be possible to use one side of the port, using 8 bits for control and a handshake line for the heat switch. I've done it the way I have for simplicity.

The final program has been done in the Pascal version only. The first change was to reassign the channel numbers for the A/D converter in an order that simplifies the handling of them later in the program. I've added an array TEMPS to hold the measured temperatures and set points for each room. The new procedure CTRL TEMP first reads all the channels and puts the readings into the TEMPS array.

I also created an array ROOM STATUS that contains the heat ON or OFF information for each room. This array is necessary because we only want to turn the heat on if the room temperature is a degree below the set point, and off if it is a degree above. That feature is the hysteresis mentioned above. The second loop in the CTRL TEMP procedure compares the temperatures and set points for each room, and sets the ROOM STATUS accordingly.

The last loop might bear some explanation. We need to "assemble" the room status information into one byte to write to the output port to turn the proper valve or baffle controls on and off. The loop runs from "7 DOWNT0 0" so that we can get the value for room 7 into bit 7 of the control byte which will be assembled in the variable VALVE. The loop counts the number of rooms that require heat, in the variable COUNT. If a room requires heat, a 1 is added to VALVE. If not, nothing is done to VALVE. In each pass through the loop, after adding one or not, the value is multiplied by 2, which is the same as a left shift by one place. After finishing the loop, the value of VALVE has a 1 in each place where a valve should be on, and a 0 for off. If you don't quite see that, a bit of dddding with a pencil and paper should clarify the point.

This is a useful technique for many sorts of control applications. The inverse may be done, by operating on a value input from a port. VALUE MOD 2 will yield a 1 if the low order bit was a 1, and 0 if it was a zero. Then VALUE DIV 2 will put the next bit in the low order position so that MOD will get the next bit, etc.

The main program remains essentially unchanged. I simply added the call to CTRL TEMP at the proper place, and changed the write statements so that they get the various temperatures from the TEMPS array rather than reading the A/D again.

Note that I've tried to keep the program understandable by not using some of the more advanced features of Pascal. I've assigned room names constant values so they could be used as array subscripts. This technique could be used in a BASIC program also though admittedly, two letter room names might be harder to understand. This program will fit a couple of 2716's in a stand alone system.

IMPORTANT NOTE: Neither I nor '68' Micro Journal will be responsible for any damages that might result from the use of this program or one patterned after it. The user will take full responsibility for the fitness of the program for the application to his heating system. If you attempt such a system and are not completely familiar with furnace controls, consult a heating contractor to be absolutely certain that you do not defeat the safety controls on your furnace. The reason for presenting this particular program was to present a reasonably simple control application with which most readers might easily relate. I personally have not built such a system, and presently don't intend to do so. I don't guarantee that there are no bugs in the program as presented here.

More on PL9

I have continued correspondence with Windrush Micro Systems regarding their PL9 compiler. I've found a few more bugs, though fairly obscure ones. Windrush has responded by return mail (as quickly as that is possible round trip to England), and in general the fixes have been a matter of a few bytes of patch. All patches have been completely successful.

I received a letter yesterday from Graham Trott, the author of PL9. I feel that we are "kindred spirits", having both gotten into computing via the simple single board microprocessor system. I have an observation (I'm editorializing here) that those who enter computing from that direction tend to think simple. Graham has written a compiler about as capable as most of the Pascal and C implementations, though admittedly requiring a bit more thought on the part of the programmer. His compiler is a single file containing the compiler proper, and an editor roughly as capable as the TSC EDIT program. It is only about 60 sectors (less than 15K!)

I have found that those who have entered computing via experience with mainframe computers, generally have trouble thinking small enough to get a compiler into "only 56K". Roughly quoting one supplier of a "tiny" C compiler, "The compiler occupies nearly all of 56K of memory, and we therefore have no plans for implementing further features of C in the future". This company's "printf" library routine was so inefficient that it generated more code than the entire runtime package.

Funny how some simple minded folks can do the impossible (like the chess game that runs in a KIM-1 single board computer in 1K of RAM). To misquote something I saw in the third book of Murphy's Laws: To write a very complex program is an easy task. To write one that is simple is a very complex task.

Off my soapbox and back to facts... I have written a couple of significant programs in PL9. I found that the

first, which runs about 8 pages of source, compiled in 32 seconds, including output to a binary file. It generated all of 5.6K of output code, or about 700 bytes per page. A good Pascal version generated about 1K per page. I have to report that this is the most efficient compiler I have found to date. That is partially true because of the easy access the programmer has to the program. He may do a number of things to optimize the code generation that are not possible with many of the more standard languages. Perhaps some day I will print some more specific examples here.

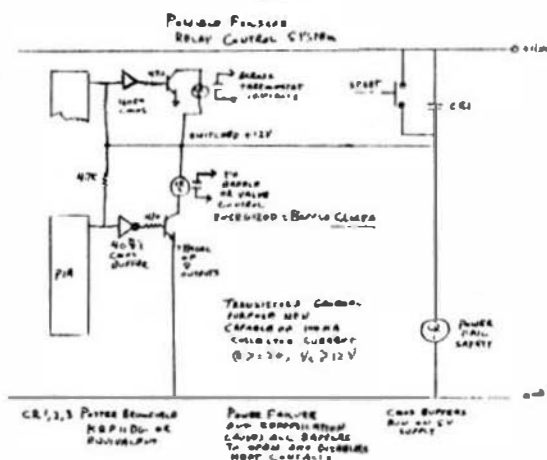
Gimix DMA

I have made an arrangement with a friend to try out a GIMIX DMA disk controller board. We traded temporarily, my SWTP DMAF2 for his GIMIX. I had one major trouble. All my disks were formatted via SWTP's "Extra Density" format, which yields 4408 sectors on an 8" disk, by squeezing two extra sectors on each track. Gimix FLEX wouldn't read those disks, and I had to borrow my DMAF2 back and format all my disks in the normal double density mode. I of course took advantage of that to get my disks organized a bit. Since formatting and copying take so long, I was able to get my computer area cleaned up and work on the basement a bit for most of the day during which I did the copy.

At any rate, once I had compatible disk formats, I found the GIMIX board very nice to use. I was able to connect two 5" drives along with my 8" drives, and for the first time, have four drives present on boot up and all capable of double density. With this board and GIMIX FLEX, the user can format both five and eight inch disks in any format, i.e. single density, double density, single or double sided, and any number of tracks up to the limit for a particular disk size, i.e. 80 tracks for 5" disks and 77 for 8". That brings up an interesting question. Why hasn't someone come up with a double track density 8" disk drive, which would hold 2 Megabytes in the DSDD mode? Perhaps 8" drives will soon be obsolete. The 80 track DSDD 5" drives already are capable of about 750K bytes of storage. Anyway, I have been using the GIMIX board for a month or so now, and I don't recall a single retry in reading or writing a disk file.

Forth Again

I just received my April '68' in which the letter from Gary Bergstrom and my reply were published. I received another letter from Gary a few days ago. I must say that he is persistent and factual in his replies. All I am going to report here is that Gary has clarified what he means by extensibility to me, and that neither of us has changed our opinions by very much. By now you have all gotten tired of the discussion, and I will say little more on the subject. We'll leave it to the people who are really enthusiastic about Forth to write some articles about it.



OS9 USER NOTES

By: Peter Dibble
517 Goler House
Rochester, NY 14620

A large number of the exciting things that can be done with OS-9 involve processes. Every program running under OS-9 is a process. Each process runs as if it had the machine to itself (except for speed). When a new process is started, OS-9 loads the Program module for the process. If it isn't already in core, creates a Process Descriptor for it, allocates the necessary amount of memory, gives it standard input and output files, and lets the new process go. One of the ongoing tasks of the operating system is to divide processor time between all processes so that the system's resources are used as efficiently as possible, and all the processes run without too many noticeable jerks. You can tell OS-9 to favor a process by giving it a high priority (with the SETPR command), or you can give a process a low priority if you don't much care how quickly it runs.

A new process is created with the OS-9 service request F\$Fork. Before issuing this service request you must set up the registers as follows:

X Address of the name of the module you want to FORK or the file that contains the module.

Y The size of the parameter area.

U The beginning address of the parameter area.

A The Language/Type code. That is, the type of module you want to fork. Basic09 has to be treated differently from object code.

B The amount of optional storage to give the new process. The interesting thing here is that it is possible to send a block of data to the new process using the parameter area. The new process will be started with X pointing to the start of a copy of the parameter area and D containing the length of the parameter area. In languages other than assembler, the parameter area can be found by noting that the parameter area is the place where the shell places the command line parameters for a program. The shell usually starts programs by FORKING them, so in any language, if you can get to the command line parameters, you can get at parameters passed through Fork in the same way.

By using the parameter area you can pass a lot of information to a new process, but you can't get anything back through the parameter area. Remember that the parameter area gets copied into the new process's address space. It is like a Pascal pass-by-value parameter -- changes don't get back to the invoking process. Still, for many jobs, the one time, one way communication afforded by the parameter area is sufficient.

Neither Basic09 nor Pascal has all the necessary functions for dealing with forked processes, but they can be reached through assembly language subroutines. I have included two short assembly language subroutines which should help. StrtTask, and WaitTask are meant to be called from Basic09, though modified versions could be called from Pascal or any other normal language. StrtTask starts execution of a process, and WaitTask waits until a child of the calling process completes before returning to the caller. These aren't examples of elegant coding, but they are good enough to play around with from Basic09. The Basic09 programs Driver, and BTest are respectively a driver for the assembly language modules and a stub for testing them.

StrtTask is an interface between a Basic09 program and the OS-9 Fork service request.

Normally, a fork is done with the SHELL statement in Basic09. By using StrtTask instead of SHELL to start "child" processes, a program can gain better control of the parameters. StrtTask allows full control of the F\$fork system service request.

The first parameter which StrtTask expects is the name of the module to be started. It should be passed as a character string with a terminator, such as a space or carriage return, after the last character of the module name. If the module might not be in memory, the name of the file which should be loaded to get the module should be the first parameter instead of just the module's name. The F\$Fork system service request description in the OS-9 System Programmer's Manual has more details about this, and all the other parameters for StrtTask.

The second parameter is the process number of the new task. It is a byte field which need not be initialized. StrtTask will place the process number of the newly started process in this byte. This is the only parameter which is returned from StrtTask. The process number is useful if you want to communicate with the new process, or to wait for a particular process to complete.

The third parameter is the language/type byte which describes the module you want to run as a child process. The easiest way to discover the proper value for this byte is by checking the module you want to fork. You can see the language/type byte for a module by loading it and doing a MOIR E command, or by doing a IDENT command on the file the module is in. Remember that this byte is displayed in hex. Object code programs (generated from assembly language) generally have a language/type byte of \$11, or decimal 17.

The fourth and fifth parameters are the length of the parameter area to be passed to the forked process, and the parameter area itself. The parameter area can be any type of data you want to pass to the new process. The length of the parameter area is passed as an integer. If you invoke a module which is usually started from the shell, the parameters should be a character string terminated with a carriage return. If you want to invoke a module which runs under Basic09, it is particularly important to include the carriage return at the end of the parameter area (which contains the name of the Basic09 l-code module to run and any parameters for it). Strange things happen if you don't.

The last parameter is the amount of optional storage space you want to give the new process. This is the number usually placed after the "n" on a shell command line. The number can range from zero to 255 (it is a byte field), and may only be in units of pages, not Kbytes.

If the fork service request itself gets a bad return code, it will be returned to the calling program as an error. In general the new process will still be running when StrtTask returns to the calling program, so there is no way to know what the completion code of the new process is (going to be).

Sometimes you may want to start a process going and continue without waiting for the new process to complete, but you may need to wait for it to complete at some point. This is where WaitTask comes in. WaitTask will wait (just sit there) until one of its children (a child of the program that called WaitTask) completes. If there are several children, the first one to complete will let WaitTask return to its caller. If there are no children, WaitTask will return with an error. If a child process terminates before it is waited for,

[illegible]

That is a very impressive commitment! If you have several users on your system with different types of terminal, you can get DYNACALC to support them all concurrently if you have each terminal type use a different data directory, and put the appropriate terminal file in each directory.

DYNACALC can save the contents of a spread sheet in a file that can be read by other programs. I wouldn't call the files easy to use, but they aren't impossible to use either, and the format is clearly documented. DYNACALC's saved data is hard to use because the format of the file reflects DYNACALC's flexible attitude towards the user - it will take any sort of data scattered around anywhere you like. If you want to create a file for DYNACALC to use as data for a spread sheet, you don't have to cope with the vicissitudes of humans. It is a relatively simple job to create data files for DYNACALC.

An excellent help facility is an integral part of the program, though you can remove it to save space if you want. Most of the time you can type a "?" to access a screen of terse explanations of your options. The help screens do not take the place of reading the manual, but they can provide a quick jog of the memory. There are also 12 error codes which I wish all visiclones had. Spread sheets can take on some of the attributes of complicated programs, especially hard to find bugs. Imagine trying to debug a program with only one error message like "Sorry, I can't do that," "Say What?" or whatever.

My copy of DYNACALC came with terminal files called:

ct 82	ct 82 92	c8200
c8200 92	c82w 92	h 1400
h 1420	h 1500	add5 vpt
h 19	ac7 lv	adm 3a
tv 912	pe 550	inf 100
tv 950		iq 120

I recognize SWTPC, Hazeltine, Adds, Heathkit/Zenith, ADM, and Televideo in there. Even if your terminal isn't in that list, you can use the INSTALL.DC utility to build a terminal file for your terminal.

A particularly strong point of DYNACALC is the set of powerful functions it supports, including basic math (trig, log/exp, square root, max/min, pi, int, round, and absolute value), "group" functions (sum, average, standard deviation, net present value, choose, lookup, and index), and a bunch of miscellaneous functions. Choose selects the nth entry from a list, lookup is the standard visiclone lookup function, and index is like lookup except that it scans for an exact match instead of greater than. Many of DYNACALC's functions work with either character strings or numbers. This expands the usefulness of the functions substantially.

DYNACALC has commands which move rows and columns around, and do insert and delete operations on them. The fanciest command in this family is the sort command, which allows you to sort rows or columns based on the values in a column or row respectively.

I have never been entirely pleased with the speed of any program. Of course I wish DYNACALC ran faster, but I don't remember using a spread sheet program on a microcomputer that ran faster.

Limitations and Problems

The only real problem with DYNACALC is with its terminal support, and I'm not sure it could have been done much better without losing generality. The terminal support problem is not a major one. In fact, I imagine that after a few months of using the program I will feel nothing but affection for it.

It is hard to choose characters to use as arrow keys. DYNACALC uses curly and square brackets as cursor control keys by default. This is a good choice if you want to drive it with a disk file, but not very intuitive. If you like this choice as little as I did you can change it with INSTALL. Unfortunately install only allows you to use single characters as control keys; my terminal, like most terminals, sends escape sequences when the arrow keys are pressed.

Screen updating is not as fast and smooth as it is on machines that have integral screen support. I understand that a 9600 baud terminal can't possibly compete with memory mapped video, but I believe that, if the insert and delete character and line facilities on my terminal were used, the screen could be updated more quickly. It would have been hard to make DYNACALC support more advanced terminals while still supporting "dumb" terminals, but I wish it had been done.

Summary

DYNACALC is a fine program, but although it seems to have been written by a programmer familiar with OS-9, it doesn't make the fullest use of the power of OS-9. I wish DYNACALC could use all available memory instead of just 64K, and I wish printing was handled by a separate process so I could start a copy of a sheet printing, then continue work on the original. Extended memory probably could have been used under Level Two without degrading the program under Level One, and multiple processes are supported by both levels of OS-9.

I find myself expecting a great deal of DYNACALC. My carping at its terminal support (which is in many ways unusually good), and pushing for support of fancy OS-9 features is a reflection of my very high opinion of the program.

I know people who find it reasonable to buy a personal computer just to have an electronic spread sheet. DYNACALC is an excellent spread sheet program. It can help with any number of business problems, simple problems in the sciences, and just plain showing off the computer to the uninitiated. I think DYNACALC is a program which should be included in the toolkit of most OS-9 users. One warning, spreadsheet programs tend to be popular. I am afraid that I will have to wait for a crack at my machine more often now that I have DYNACALC on it.

DYNACALC, all versions OS9™, FLEX™ and UNIFLEX™ version are available from:

SE MEDIA
PO Box 794
Hixson, TN 37343
(615) 842-4601

- - -

Editor's Note: It should be noted that a lot of the work of creating a spreadsheet can be eliminated by the use of the many spreadsheet 'how-to' books, available at most computer book outlets. DYNACALC is so 'like' the other popular programs that the examples are very easy to carry over. Fact is you will have an advantage, DYNACALC does MORE than most of the others. In my personal opinion, DYNACALC is one of the most powerful programs to ever run on any computer. We know of many Standard S50 Bus computers that have been sold just to use DYNACALC. Should tell you something.

DMW

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O-F (REVIEW - OS9 TO FLEX OS9)

by Peter C. Dibble
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A few weeks ago I spent most of a Saturday hooking my old SWTPC FLEX machine to my new machine as a remote computer so I could use it to write a FLEX-format disk. It felt rather odd using my "smart terminal" program to communicate with a machine less than a foot away. The process involves shuffling disks drives back and forth, and much opening and shutting of cabinets. I don't like it much. My new machine has GIMIX software switching, so I can run FLEX on it, but even the remarkable GIMIX CPU board can't run both operating systems at once. On occasion I have uploaded a file from one OS to an IBM and then downloaded it with the other OS, accomplishing a change of disk format from FLEX to OS-9 or vice-versa. These methods are all inelegant, ad hoc solutions to a problem. Dr. Matthew Scudiere has come up with a much cleaner solution: He has written an OS-9/FLEX copy program called O-F.

General System Description

This OS9/FLEX copy program is a BASIC09 program which allows the user to convert an OS-9 format disk into a hybrid form which can be read and written by FLEX. In the process of doing this it makes the disk inaccessible to OS-9 except as an entire disk (i.e. /Dn#) but O-F is able to copy files to and from the hybrid disk, and read the FLEX directory. The disk that results from the reformatting is enough like standard FLEX format that FLEX doesn't know the disk isn't one of its own.

Limitations

Only freshly formatted, single sided 5 or 8 inch disks with no bad sectors can be used, and there is no way to use a disk which is in real FLEX format (formatted by the FLEX NEWDISK or FORMAT program). The FLEX to OS-9 copy part of the program expands tab characters into strings of blanks by default, but there is an option which causes the file to be copied intact. Of course, this program doesn't make any attempt to convert FLEX programs into OS-9 programs. That is work for other programs.

Operation

In order to run O-F you must first start Basic09. The version I tested was in source form, so I had to load it and run it. If it is distributed as Basic09 i-Code it should be possible to just run it. The program lists 7 options:

- 0 Directions
- 1 FLEX Directory
- 2 Copy FLEX text file to OS9
- 3 Copy OS9 path to FLEX
- 4 Delete FLEX File
- 5 Reformat OS9 Disk
- 6 Exit program and prompts for a selection.

"Directions" produces a quick summary of the function of the program, about half a screen full. "FLEX Directory" lists the basic information in the directory of a pseudo-FLEX disk: file name, Begin, End, Size, and date. It also gives the number of sectors used on the disk, and the number of sectors left. The "Copy FLEX text file to OS9" dialogue is:

FLEX Compatible source Drive ID --

FLEX file name to copy --

Copy to OS9 destination path -- The "Compatible source Drive ID" is the device name for the disk that has been reformatted; that wasn't too clear to me. The "Copy OS9 path to FLEX" dialogue is:

Drive ID --

FLEX File name to write (Use upper case) --

Copy FROM OS9 SOURCE path -- To delete a FLEX file, select 4, then:

Flex compatible source Drive ID --

FLEX file name to delete (use proper case) --

The dialogue for reformatting a disk is very cautious:

Drive ID --

Are you sure? --

Overwrite -- <old volume name here>

Are you sure? --

5-1n or 8-1n disk?

I tried reformatting and writing on 5 inch disks (SS/SD, SS/DD, 40 track and 80 track), and 8 inch disks of all permutations. It worked on the 5 inch disks, and on SS single and double density 8 inch disks. I was able to read pseudo-flex files created by O-F from FLEX without any trouble. O-F had no trouble reading files written by FLEX on disks reformatted by O-F. The reformatted disks were also fully usable in FLEX. FLEX truly thinks the reformatted disk is one of its own. One nice touch is that the program puts two entries in the OS-9 root directory of the reformatted disk:

** NO OS9 Files Allowed ** (This is a FLEX copy disk)

These entries appear if you do a DIR command on the reformatted disk, letting you know very quickly that this disk is special.

Evaluation

This is a competent and very useful program. It is especially well equipped with error messages and informative text. In fact, although it came without a manual, I was able to follow the built-in directions without any trouble. I do hope that a manual is available by the time this program hits the market. A program without a manual seems somehow unbalanced even if it is usable without documentation. A nice extra is that it appears that this program may be distributed in source form.

O-F works by tricking FLEX. This together with the variety of disk formats that FLEX might use forces the program have some odd restrictions. The most serious limitation is the restriction to specially formatted disks. It certainly would be nice to be able to drag out a four year old FLEX disk and read it with this program. The restriction to single sided disks is reasonable in the context of copying files from one format to the other. For some people the most important limitation will be the language requirement. Since this is a Basic09 program, you must have Basic09 to be able to run it. It could be a measure of the desperate need for a program like this one that it is being hustled out in Basic09 form.

One of O-F's strongest points is the cautious approach it takes to the user. This program doesn't know how to deal with double sided disks, but it doesn't just tell you so, it won't let you use them. You get a message clearly telling you that double sided disks are not-ok if you try. Similar messages appear if you try to use a disk that is flawed in a number of other ways.

Summary

O-F is available from SE MEDIA. It isn't really a program of general interest ... there are probably some OS-9 users who don't have FLEX or friends with FLEX. Those people have very little use for this program. The group of people this program should prove most useful to are the owners of software-switching machines. Using this program they can conveniently transfer data between

operating systems. There are a lot of FLEX users out there -- our close relatives in the computer world. It is good to be able to exchange disks with them even if we have to be the ones to provide the disks.

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A/D CONVERTER - JPC INTERFACE

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AN ANALOGUE TO DIGITAL CONVERTOR PATCH FOR MICROWARE SYSTEM CLOCK MODULE

INTRODUCTION

It became necessary in the course of interfacing a human calorimetry system to a GIMIX 6809 System with 58K of memory and two small 5 1/4 inch discs to be able to convert eight channels of analogue data and one channel of digital pulse data for use by a basic program to perform certain calculations in order to compute oxygen consumption, carbon dioxide elimination and respiratory quotient. Also, weight, patient temperatures, two wet bulb and two dry bulb temperatures were needed for the computations which were to be outputted each minute or a multiple thereof. The pulse input was needed to measure total airflow through the system using a dry test meter that closed + clock-1 (or opened) a contact every liter of air flow. The details of the interface and the calorimetry system need not be discussed here except to point out these signals were conditioned to be in the range of 0 - 5 volts.

METHODS

A JPC Products Company AD - 16 analogue to digital conversion kit was purchased and assembled, tested with a basic program and worked entirely satisfactorily. A Motorola MC 6840 timer was used to measure the period of 100 pulses and thus enable the basic program to compute flow. To summarize, the requirements were to be able to convert eight channels of analogue data, store the results, move the results to a safe place for basic to read and convert the same eight channels one minute later (of course, this can be varied in the program if desired). Also the period of 100 pulses needed to be measured so flow could be determined.

RESULTS

```
00001      XAM Clock Module
00002      IFPI
00003      ENDC
00004      Use /dd:deis/systype
00005
00006      * SYSTEM DEFINITIONS
00007      E200 A.IERN EQU $E050
00008      E201 CPART EQU $E220
00009      E003 PPT EQU 3
00010      E002 PC6840 EQU 2
00011      E001 MSB167 EQU 1
00012      E001 CLK17P SET MSB167
00013      E002 MHZ2 EQU 2
00014      E001 MHZ1 EQU 1
00015      E002 CPUSPD SET MHZ2
00016      E000 END

00019      *****
00020      *
00021      * CLOCK MODULE
00022      *
00023      * INITIALIZE MSB167 CLOCK CHIP FOR 100MS INTERVALS
00024      * AND SETS IRQ POLLING ROUTINE
00027      *
```

```
00020      *****
00021      *
00022      * MODULE HEADER
00023      *
00024      E000 07C00231 C1Mod Add C1End,C1Name,Type,Rev,C1Ent.CPORT
00025      E000 0000 ORG 0
00026      E000 0000 BUFFER RND 36
00027      E000 0000 BUPYR RND 2
00028      E000 0000 FRTPYR RND 2
00029      E000 0000 CDMT RND 1
00030      E000 0000 ADATA RND 1
00031      E000 0000 SAVMT RND 2
00032      E000 0000 SAVMT RND 2
00033      E000 0000 SECND RND 1
00034      E000 0000 COUNTS RND 1
00035      E000 0000 T1 RND 2
00036      E000 0000 T2 RND 2
00037      E000 0000 T3 RND 2
00038      E000 0000 T4 RND 1
00039      E000 0000 T5 RND 1
00040      E000 0000 ENDBUF EQU
00041      E000 0000 C1Name FCS /Clock/
00042      E000 0000 CLKPRM EQU MSSTAK Edition number
00043      E000 0000 Stack has Clock Port address
00044      *
00045      * CLOCK DATA DEFINITIONS
00046      *
00047      E000 0013 TNSVC FCB FTIME
00048      E000 0014 DDFS FCB TIME-2
00049      E000 0015 DDFS FCB 000
00050      E000 0000 SecMilli EQU 0
00051      E000 0001 SecTenth EQU 1
00052      E000 0002 Second EQU 2
00053      E000 0003 Minute EQU 3
00054      E000 0004 Hour EQU 4
00055      E000 0005 DayWeek EQU 5
00056      E000 0006 DayMonth EQU 6
00057      E000 0007 Month EQU 7
00058      E000 0010 Status EQU 16
00059      E000 0011 Control EQU 17
00060      E000 0012 CountRst EQU 18
00061      E000 0013 LatchRst EQU 19
00062      E000 0014 PollOver EQU 20
00063      E000 0015 Gd EQU 21
00064      E000 0016 PATREG EQU $E050
00065      E000 0017 STATREG EQU $E051
00066      E000 0018 D2GREG EQU $E052
00067      E000 0019 CNTREG EQU $E053
00068      E000 0020 TERNEN EQU $E054
00069      E000 0000 CRJ3 EQU 0
00070      E000 0001 CRJ5R EQU 1
00071      E000 0002 MS011 EQU 2
00072      E000 0003 FILSD EQU 3
00073      E000 0004 MS012 EQU 4
00074      E000 0005 T2LSD EQU 5
00075      E000 0006 MS013 EQU 6
00076      E000 0007 T3LSD EQU 7
00077      E000 0010 FLOPRM EQU $E210
00078      E000 0017 4144045 MODNAM FCS /ADDRESS/
00079      E000 0011 MODTYP SET PRSRV+OBJECT
00080      E000 001F 10 FORMAT FCB 10 CHANNEL 1
00081      E000 0020 11 FCB 11 CHANNEL 2
00082      E000 0021 12 FCB 12 CHANNEL 3
00083      E000 0022 13 FCB 13 CHANNEL 4
00084      E000 0023 14 FCB 14 CHANNEL 5
00085      E000 0024 15 FCB 15 CHANNEL 6
00086      E000 0025 16 FCB 16 CHANNEL 7
00087      E000 0026 17 FCB 17 CHANNEL 8
00088      *
00089      E000 0027 27 ENDFMT FCB ENDFMT
00090      *
00091      *****
00092      *
00093      * CLOCK INTERRUPT SERVICE ROUTINE
00094      *
00095      E000 0020 6E9F0030 NOTCLK JMP 10.ISVC3 GO TO INTERRUPT SERVICE
00096      E000 0020 00E210 CLKSrv LBI 0FLOPRM GET COUNTER ADDRESS
00097      E000 0020 A601 LDA 1,X READ STATUS REG.
00098      E000 0031 0004 BITA 0504 IS IT COUNTER 03
00099      E000 0031 2602 BNE FLOTRQ
00100      E000 0031 202E BRA CLKSrv1
00101      E000 0037 FE0000 FLOTRQ LBI 0TERMEN
00102      E000 003A A601 LDA 1,X
00103      E000 003C EC06 LDB 6,X READ T3
00104      E000 003E EBC040 STB T3,U
00105      E000 0041 A601 LDA 1,X
00106      E000 0043 EC04 LDB 4,X READ T2
00107      E000 0043 EBC044 STB T2,U SAVE T2
00108      E000 0040 A601 LDA 1,X
00109      E000 0040 EC02 LDB 2,X READ T1
00110      E000 004C EBC044 STB T1,U SAVE T1
00111      E000 004F 0603 LDB 0003
00112      E000 0051 A701 STA 1,X
00113      E000 0053 0601 LDB 0,X RESET TIMERS
00114      E000 0053 A704 STA 0,X TO BE SURE THAT
00115      E000 0057 0600 LDB 0,X ALL TIMERS ARE INITIAT.
00116      E000 0059 A704 STA 0,X BEFORE STARTING AGAIN
00117      E000 0050 AEDFFAC LDI CLKPRM,PCR
00118      E000 005F A6010 LDA STATUS,1
00119      E000 0062 260A BNE CLKSrv2
00120      E000 0044 30 RTI
00121      E000 0065 AEDFFA2 CLKSrv1 LDI CLKPRM,PCR GET CLOCK ADDRESS
00122      E000 0067 A6010 LDA STATUS,1 GET STATUS/CLEAR INTERRUPT
00123      E000 006C 270A BEQ NOTCLK BRANCH IF NOT CLOCK
```

```

00164 00165 W 205E F00000 CLKSrv2 LDU >2EPHEN SO TO BUFFER SWIFT
00166 00167 0075 2659 L0A 0059 ON 59TH SECOND
00168 00169 0075 A102 C9PA Second.1
00170 00171 0075 2707 REG TICK1 PREVENTS GOING
00172 00173 0075 2807 L0B 010 TO CLRBUF 10 TIMES
00174 00175 0075 2907 STB COUNTS,U DURING 59TH SECOND
00176 00177 0075 3007 BPA TICK JMP TO CLRBUF
00178 00179 0075 3107 TICK1 DEC COUNTS,U ONLY AFTER 10TH PASS
00180 00181 0075 3207 L0B0 CLRBUF
00182 00183 0085 4F TICK CLPA SET DIRECT PAGE
00184 00185 0085 1F0B TFR A,BP
00186 00187 0085 1F0117 L0B0 ASCIRO
00188 00189 0085 6E7FFFE0 TICK50 JMP GO TO SYSTEM CLOCK ROUTINE
00190 00191 0000 0000 0000 0000
00192 00193 0000 0000 0000 0000
00194 00195 0000 0000 0000 0000
00196 00197 0000 0000 0000 0000
00198 00199 0000 0000 0000 0000
00200 00201 0000 0000 0000 0000
00202 00203 0000 0000 0000 0000
00204 00205 0000 0000 0000 0000
00206 00207 0000 0000 0000 0000
00208 00209 0000 0000 0000 0000
00210 00211 0000 0000 0000 0000
00212 00213 0000 0000 0000 0000
00214 00215 0000 0000 0000 0000
00216 00217 0000 0000 0000 0000
00218 00219 0000 0000 0000 0000
00220 00221 0000 0000 0000 0000
00222 00223 0000 0000 0000 0000
00224 00225 0000 0000 0000 0000
00226 00227 0000 0000 0000 0000
00228 00229 0000 0000 0000 0000
00230 00231 0000 0000 0000 0000
00232 00233 0000 0000 0000 0000
00234 00235 0000 0000 0000 0000
00236 00237 0000 0000 0000 0000
00238 00239 0000 0000 0000 0000
00240 00241 0000 0000 0000 0000
00242 00243 0000 0000 0000 0000
00244 00245 0000 0000 0000 0000
00246 00247 0000 0000 0000 0000
00248 00249 0000 0000 0000 0000
00250 00251 0000 0000 0000 0000
00252 00253 0000 0000 0000 0000
00254 00255 0000 0000 0000 0000
00256 00257 0000 0000 0000 0000
00258 00259 0000 0000 0000 0000
00260 00261 0000 0000 0000 0000
00262 00263 0000 0000 0000 0000
00264 00265 0000 0000 0000 0000
00266 00267 0000 0000 0000 0000
00268 00269 0000 0000 0000 0000
00270 00271 0000 0000 0000 0000
00272 00273 0000 0000 0000 0000
00274 00275 0000 0000 0000 0000
00276 00277 0000 0000 0000 0000
00278 00279 0000 0000 0000 0000
00280 00281 0000 0000 0000 0000
00282 00283 0000 0000 0000 0000
00284 00285 0000 0000 0000 0000
00286 00287 0000 0000 0000 0000
00288 00289 0000 0000 0000 0000
00290 00291 0000 0000 0000 0000
00292 00293 0000 0000 0000 0000
00294 00295 0000 0000 0000 0000
00296 00297 0000 0000 0000 0000
00298 00299 0000 0000 0000 0000
00300 00301 0000 0000 0000 0000
00302 00303 0000 0000 0000 0000
00304 00305 0000 0000 0000 0000
00306 00307 0000 0000 0000 0000
00308 00309 0000 0000 0000 0000
00310 00311 0000 0000 0000 0000
00312 00313 0000 0000 0000 0000
00314 00315 0000 0000 0000 0000
00316 00317 0000 0000 0000 0000
00318 00319 0000 0000 0000 0000
00320 00321 0000 0000 0000 0000
00322 00323 0000 0000 0000 0000
00324 00325 0000 0000 0000 0000
00326 00327 0000 0000 0000 0000
00328 00329 0000 0000 0000 0000
00330 00331 0000 0000 0000 0000
00332 00333 0000 0000 0000 0000
00334 00335 0000 0000 0000 0000
00336 00337 0000 0000 0000 0000
00338 00339 0000 0000 0000 0000
00340 00341 0000 0000 0000 0000
00342 00343 0000 0000 0000 0000
00344 00345 0000 0000 0000 0000
00346 00347 0000 0000 0000 0000
00348 00349 0000 0000 0000 0000
00350 00351 0000 0000 0000 0000
00352 00353 0000 0000 0000 0000
00354 00355 0000 0000 0000 0000
00356 00357 0000 0000 0000 0000
00358 00359 0000 0000 0000 0000
00360 00361 0000 0000 0000 0000
00362 00363 0000 0000 0000 0000
00364 00365 0000 0000 0000 0000
00366 00367 0000 0000 0000 0000
00368 00369 0000 0000 0000 0000
00370 00371 0000 0000 0000 0000
00372 00373 0000 0000 0000 0000
00374 00375 0000 0000 0000 0000
00376 00377 0000 0000 0000 0000
00378 00379 0000 0000 0000 0000
00380 00381 0000 0000 0000 0000
00382 00383 0000 0000 0000 0000
00384 00385 0000 0000 0000 0000
00386 00387 0000 0000 0000 0000
00388 00389 0000 0000 0000 0000
00390 00391 0000 0000 0000 0000
00392 00393 0000 0000 0000 0000
00394 00395 0000 0000 0000 0000
00396 00397 0000 0000 0000 0000
00398 00399 0000 0000 0000 0000
00400 00401 0000 0000 0000 0000
00402 00403 0000 0000 0000 0000
00404 00405 0000 0000 0000 0000
00406 00407 0000 0000 0000 0000
00408 00409 0000 0000 0000 0000
00410 00411 0000 0000 0000 0000
00412 00413 0000 0000 0000 0000
00414 00415 0000 0000 0000 0000
00416 00417 0000 0000 0000 0000
00418 00419 0000 0000 0000 0000
00420 00421 0000 0000 0000 0000
00422 00423 0000 0000 0000 0000
00424 00425 0000 0000 0000 0000
00426 00427 0000 0000 0000 0000
00428 00429 0000 0000 0000 0000
00430 00431 0000 0000 0000 0000
00432 00433 0000 0000 0000 0000
00434 00435 0000 0000 0000 0000
00436 00437 0000 0000 0000 0000
00438 00439 0000 0000 0000 0000
00440 00441 0000 0000 0000 0000
00442 00443 0000 0000 0000 0000
00444 00445 0000 0000 0000 0000
00446 00447 0000 0000 0000 0000
00448 00449 0000 0000 0000 0000
00450 00451 0000 0000 0000 0000
00452 00453 0000 0000 0000 0000
00454 00455 0000 0000 0000 0000
00456 00457 0000 0000 0000 0000
00458 00459 0000 0000 0000 0000
00460 00461 0000 0000 0000 0000
00462 00463 0000 0000 0000 0000
00464 00465 0000 0000 0000 0000
00466 00467 0000 0000 0000 0000
00468 00469 0000 0000 0000 0000
00470 00471 0000 0000 0000 0000
00472 00473 0000 0000 0000 0000
00474 00475 0000 0000 0000 0000
00476 00477 0000 0000 0000 0000
00478 00479 0000 0000 0000 0000
00480 00481 0000 0000 0000 0000
00482 00483 0000 0000 0000 0000
00484 00485 0000 0000 0000 0000
00486 00487 0000 0000 0000 0000
00488 00489 0000 0000 0000 0000
00490 00491 0000 0000 0000 0000
00492 00493 0000 0000 0000 0000
00494 00495 0000 0000 0000 0000
00496 00497 0000 0000 0000 0000
00498 00499 0000 0000 0000 0000
00500 00501 0000 0000 0000 0000
00502 00503 0000 0000 0000 0000
00504 00505 0000 0000 0000 0000
00506 00507 0000 0000 0000 0000
00508 00509 0000 0000 0000 0000
00510 00511 0000 0000 0000 0000
00512 00513 0000 0000 0000 0000
00514 00515 0000 0000 0000 0000
00516 00517 0000 0000 0000 0000
00518 00519 0000 0000 0000 0000
00520 00521 0000 0000 0000 0000
00522 00523 0000 0000 0000 0000
00524 00525 0000 0000 0000 0000
00526 00527 0000 0000 0000 0000
00528 00529 0000 0000 0000 0000
00530 00531 0000 0000 0000 0000
00532 00533 0000 0000 0000 0000
00534 00535 0000 0000 0000 0000
00536 00537 0000 0000 0000 0000
00538 00539 0000 0000 0000 0000
00540 00541 0000 0000 0000 0000
00542 00543 0000 0000 0000 0000

```

```

00447 01F9 2901      ADCA 1,X      ADD CARRY AND HIGH BYTE TO A
00448 01FB 2701      STA 1,X      SAVE HIGH BYTE
00449 01FD 2502      BCS OVERFLO  SAVE CARRY
00450 01FF 2007      BRA EXIT
00451 2901 49      OVERFLO ROLA      ROTATE CARRY TO BIT 0
00452 2902 9401      ANDA 02000001 SAVE CARRY ONLY
00453 2904 4004      ADRA 0,X      ADD HIGH BYTE TO CARRY
00454 2906 2704      STA 0,X      SAVE HIGH BYTE
00455 2208 39      EXIT RTS
00456 *****
00457 * SUBROUTINE TO TRANSFER BUFFERS TO SAFE
00458 * PLACE FOR BASIC TO READ AND ALSO
00459 * CLEAR ALL BUFFERS
00460 *****
00461 M 0209 FE0000  CLRBUF LBU >ZERHEN GET ADDRESS
00462 020C 6CC84A  INC TIMER,U
00463 020F 6CC84B  INC TIMER2,U
00464 0212 21C4  LEAY BUFFER,U AGO. OF START OF BUFFER
00465 0214 10AFCB38 STY BUFPTR,U SAVE IT
00466 0218 C61B  LDB 024 SET UP COUNTER
00467 021A E7C83C  STB COUNT,U GET COUNT TO STOP ON
00468 021D E6A4  LDB 0,Y GET BYTES
00469 021F 6FA4  CLR 0,Y
00470 0221 E7AB1B  STB 27,Y STORE BYTES AHEAD
00471 0224 3121  LEAY 1,Y
00472 0226 5ACB3C  DEC COUNT,U
00473 0229 26F2  BNE CONT
00474 022B 16FE57  LDBRA T1CX
00475 022E 37B700  ENDB
00476 0231  CltEnd  *
00477 0231  opt c
00479

```

DISCUSSION

In order for the analogue to digital convertor and pulse timer to provide the data for a basic program memory space had to be allotted. This was done by giving the memory module a name (modnam) and type (modtype) as shown at decimal line 111. At line 370 the OS9 request F\$LINK is used to return the memory address in the "Y" register. This address was next stored at memory location 0000 where it was accessible both to basic and any other programs use.

The M58167 clock interrupts every 100 milliseconds and even if there may be another interrupt the clock interrupt service routine is entered first. Immediately the MC6840 is checked to see if it is the source of the interrupt. If not the source the M58167 is checked. If it is not the source the routine branches back to the system. If the MC 6840 is the interrupt source, it is serviced, all timers read and values stored for later use. The RTI instruction at decimal 152 is to make sure the program returns to the regular interrupt routine, otherwise the system cannot find the other interrupts.

At decimal line 165 the CLKSRV2 routine prevents going to the clear and shift buffer routine ten times during the 59th second.

At line 379 the analogue to digital convertor is initialized as well as the MC6840 timer.

The routine beginning at line 410 reads the analogue to digital convertor and the routine at line 442 saves this data in triple precision format.

The routine of 461 moves these buffers down each minute and clears the original locations.

There is some redundancy in these routines but they work. It proved to be a painful debugging chore since there was no debugger to operate on top of the program.

The decimal numbers are not consecutive as many redundant assembly lines and pages have been deleted for publication.

BILL HELPS AND DOUG ROBINSON OF MICROMARE HAVE
AUTHORIZED THE PUBLICATION OF THEIR CLOCK MODULE
WITH THIS PATCH

AAA SUPER MODEM

Over the past year or so we have seen an upswing in the demand for GOOD modem programs. Several have been published in 68 MICRO JOURNAL, however, they required an additional port or used an interrupt timer extensively, other than the terminal and modem port. While this in no way detracts from the operation of the system, once it is properly installed, it can be a nightmare to those who are not inclined towards the innerds of their computer. The AAA CHICAGO COMPUTER CENTER - 'Super Modem Program' does it all, and without interrupts or additional interfaces.

When we first got our BBS on line I needed a GOOD program to use at the home computer. There I act as SYSOP and also download files to our office. Jerry of AAA sent us his Super Modem Program to use on the home system, and it has performed like a champ!

General Overview

The modem program was originally written in 6800 machine language. It has since been redone for the 6809 with up-grades. It is furnished on either 5 or 8 inch disks and comes complete with source. To me personally this is a critical part of any software package. So, you not only get a very well documented instruction book but the source code (commented) as well. The source allows you to install it either on the FLEX™ or SSB DOS™ by changing a few bytes of code, for which the instructions are furnished. Special requirements for the GIMIX™ and other video boards are covered.

Operation

When the Super Modem Program is first loaded you are in local mode. This means that anything typed at the console is local and not transmitted beyond the console CRT. Data will still be received and displayed from the remote system in this mode.

All functions are menu driven. The menu can be recalled at any time, in case you forget what key fetches what function. Entering a control D will put you back into your DOS, or exit the modem program.

The following is a short review of each function:

1. control T - entry to the transmit function. A CR/LF is your prompt that you are actually in transmit. You will remain in the transmit mode until you enter an escape, at which time you are returned to local mode.

2. control F - this function will request (and not transmit any local interaction) the name of a disk file to be transmitted. You enter the file name, no extension, default to .TXT, and a CR, transfer of your disk file to the remote system is then accomplished. If you desire you may interleave keyboard activities with the file transmission, this would be useful for adding a note or other info to the transmitted file before it is closed. Therefore, you would first enter 'control T' then enter 'control F' to transmit a disk resident file from your system to the remote.

3. control E - should the remote system require an echo upon transmission, you would use this function to place the system in echo. Entering a second control E will toggle back. Control E may be entered at any time. This allows you to echo received data back to the sending system.

4. control R - this allows the reception, into your computer memory, a file from the remote system. nulls, deletes and linefeeds are ignored. Characters entered at the keyboard will be transmitted to the remote computer. Full duplex is supported without interrupts. When the file is completed, the control Q will save it to your disk system. It prompts for a file name if the sending computer does not send a 'auto-close'. Files larger than your available memory may be received for disk storage, if sending computer supports X ON/OFF.

5. control S - toggles full and half duplex.

6. control L - add a LF when a CR is received from modem. This is required by some systems that do not generate an automatic LF on receiving a CR.

7. control A - this function allows a delay to be inserted by your system should you have 'speed' problems. In our case we have our system come up in 'A' and also we

changed the default value to one more in line with our system. While this slows down the process somewhat, it allows us to transmit data to some systems that cannot receive characters as fast as we send them (300 baud). By turning 'A' on or off you have the luxury of working with systems that other modem programs will not work with. Provisions are made for additional delay, if required.

In the event you fail to receive the echoed character from the other system, or your system stalls, press any key and the transmissions can be restarted without loss of data.

The 'escape' key will terminate the current function and return to local mode and redisplay the command menu.

The 'X-ON and X-OFF' functions are supported and the 'ON or OFF' character may be changed to suit your particular needs. Again full source code is furnished for custom applications.

Control B - X-ON
Control C - X-OFF
Control V - start disk write
Control W - end of file

In the event control characters are received, which are not printable to the screen, the program indicates them with the underscore character, on the screen, with the exception of the backspace, line feed and carriage return.

One note that is worthy of special mention is the inclusion of a flow chart of the program. This allows source code modification much simpler. This is more a 'plus' than you might believe.

We have been using this program for over a year and have not found any flaws or bugs. Some rather large files have been sent and received using the Super Modem Program and any errors have been due to bad telephone lines (at our office). The nice part is that it uses no interrupts or additional hardware, other than another port for our modem. I have modified our version to come up in transmit and a few other things that I wanted for my particular needs. Because of the commented source code and the flow chart, these have been no problem at all. Most all users who dabble in machine code and using an assembler will have little or no problems. However, it works fine, right out of the box.

There are very few things needed to start up. You need to tell it which port your modem is located on and that is about all.

PRICE: \$75.00

FROM:

AAA CHICAGO COMPUTER CENTER
120 Chestnut Lane
Wheeling, IL 60090
(312) 459-0450

COLOR User Notes

Robert L. Ney
5900 Cassandra Smith Rd.
Hixson, Tn. 37343

As usual, I'm running about 5 minutes before the deadline; this month just evaporated. I have received a lot of positive feedback on the last couple of months worth of listings; as I stated earlier, if you want the same thing for the Disk ROM, let me know. Obviously, those listings are just a beginning. What we need NOW is the info on those Subroutine Entries; Entry requirements, what it accomplishes, what Registers are affected and how, exit conditions, etc. We are hearing about a whole NEW set of ROMs for the Color Computer; Ver 1.2 BASIC, Ver 1.1 EXTENDED BASIC, and Ver 1.1 DISK BASIC that uses the full BK (\$0000 to \$0FFF). Again, we will fill you in when we see what is happening.

If you are writing Assembly Language Programs for the Color Computer, remember that Radio Shack ONLY GUARANTEES their PUBLISHED Routine Entry Points; anything else you do may not work with a new Version ROM. They are better than nothing, but it sure would be

a lot easier if they had followed some of the more mature DOS Procedures of defining firm Routine Entries for lower-level functions such as GetChar, PutChar, etc. (the 68xx standards of INCH, INCHE, INEEE, OUTEE, etc.). The High-Level functions Radio Shack provide severely restricts a programmers flexibility. The whole point is this; the Listings we just provided SHOULD be useable, especially where EXTENDED BASIC is using some of the Routines from BASIC, but they also could change, leaving a Program hanging "High and Dry". Just keep these points in mind. The only thing that SHOULD be FIRM is the Hardware Locations; if you are writing serious Software that you expect to be useable over an extended period, WRITE YOUR OWN I/O ROUTINES which interface DIRECTLY to the Hardware, and it SHOULD be safe. Maybe I am making too big a thing about this; it's just that some of us have become awful jittery when it comes to expecting any kind of consistency out of Radio Shack. Enough said!

— PROGRAMMING —

I thought I would begin a series of discussions concerning PROGRAMMING IN GENERAL. There are some excellent Books available which discuss "How to Program" with CERTAIN Languages, but very few which discuss Programming In General. This month we'll look at some OVERALL CONCEPTS and TERMINOLOGY, and get on into the meat of the thing later.

This presentation will not be directed specifically at any single Programming Language, although ANY attempt at presenting Efficient and Understandable Algorithms (big word; skip it for now if you don't understand it: I am just establishing, right up front, that I know and can use BIG Words, which indirectly leaves YOU, the impressionable READER, with the opinion that I know what I am talking about) invariably leads to a Pascal-Type of syntax and structure. Since that was the Overall Intent of Mr. Wirth and his Pascal, I suppose it is to be expected. Don't give up yet; I said GENERAL, which means this will apply to BASIC, PASCAL, C, ASSEMBLY LANGUAGE, etc. (even FORTH, to an extent).

OVERALL CONCEPTS:

Most new Programmers have the misconception that the actual Coding of a Program in the chosen Language is the HARD Part of Programming; THAT's the EASY PART of writing a Program. If the Program is properly DEFINED, it can be coded in almost ANY Language, and have a high probability of working correctly. My real complaint with BASIC, and the way it is normally presented, is that it induces the user to start right in writing Code, and requires an unacceptable amount of Debugging to get a Program running correctly. It is an extremely useful Language, and has a lot of power and capability, but it also tends to "Teach all of the WRONG Procedures" when it is the FIRST Language a user learns. If you need a quick, single shot, solution to a problem, BASIC is normally the way to go, because of its flexibility and ease of use. I also want to state, before you start throwing rocks, that the BASIC Language provided with the Color Computer is one of the MOST POWERFUL BASIC's available on ANY Computer. It has enough of the Low-Level capabilities (such as DSK1\$ and DSK0\$) to allow you to do about anything you want with it. The Operating System is rudimentary, but the BASIC is very good (if we just had a ONERROR GOTO, we would really have something).

In general, a Program is DESIGNED, CODED, and TESTED. IF it is properly designed, the coding and testing is fairly straight forward. If it is a fairly large Program, subsections can be designed, coded, and tested, and then incorporated into the overall program. The overall Program, then, is built up of "Working Modules", and you should be fairly confident of its performance. This concept provides some side benefits. It is easy to change the Program by removing and/or replacing Modules with other Modules. You can also build up a file of

Modules that accomplish specific functions, such as Binary Search, Shell Sort, etc., which can be used in other Programs. Languages such as Pascal, C, etc., were designed to use this type of "Module" Structure. Assembly Language Programmers use modules in a "Library" File; while in BASIC they can be designed as Subroutines, or as a module that you would "Merge" or Insert into a Program while you are writing it.

If you haven't heard the term "Structured Programming", you are REALLY new to the Computer scene. Structured Programming has been batted around so much that the concept has become obscured by the details. Yet, Structured Programming is the salvation of the Software Writer. You can think of Structured Programming as you would of Outlining a Letter or Article; i.e., breaking the whole into parts, where each part is a Module. This Outline concept will also get you headed in the right direction in the "Top Down" problem solving methods, also. (Whoa; no rocks! I'm trying to present CONCEPTS, not precise terminology.)

Finally, you CAN NOT EFFICIENTLY design a Program without PROPER DOCUMENTATION! In other words, KEEP GOOD NOTES about what you are doing, how you are doing it, etc. When you change a part of the Program, keep the documentation up to date. This allows you to keep up with what was changed, why, when, etc. It also greatly simplifies debugging (if you listen to the experts, you should never NEED to DEBUG, right?).

With a concept of some of the "Parts and Pieces", we can now look at how to attack a Program. In general, it will go something like this:

1. Define the DATA Structures; i.e., what is going IN, and what is coming OUT. If the Program is a Game, the Data going IN may come from the Keyboard or Joystick, and the OUTPUT may be on the Display. If we are writing an Accounting Program, the INPUT Data will be in the form of Records, with the OUTPUT being Processed Records. If you think about it, there MUST be some Data to work on if we are even going to HAVE a Program.
2. Define HOW we are going to operate on this Data. This will be the different "Algorithms", or Procedures that we will use to accomplish different things. The "Top Down" approach to Problem Solving consists of breaking a large problem down into smaller and smaller parts, until we are at the point where the CODE to accomplish this part is OBVIOUS (that is why Coding is the EASY part). Some Programming Languages, such as FORTH, require that you define any Procedure BEFORE you use it, so it has to be coded "Bottom UP"; but you STILL break the problem DOWN using the "Top DOWN" approach.
3. TEST the Program; i.e., run it and see where smoke appears. IF you have decent DOCUMENTATION (for reference, if needed), and have PROPERLY accomplished (??) the preceding two steps, testing should be as simple as finding some local idiot that has NEVER SEEN a Computer and see if he/she/or it can run/break the Program. Simple, huh?
4. Now for the HARD PART; FINALIZE THE DOCUMENTATION. If it is properly done, you can hand it to someone that knows the Language that the Program is written in, and THEY can EASILY add to your Program, debug your Program, etc. Remember, a month from now, YOU won't remember all of the details of the Program.

TERMINOLOGY

Some of the Terminology of Programming has already been mentioned, and you have a idea of what it means. Again, I am not going to attempt to be extremely PRECISE; I want to get the "Ideas" or "concepts" across, so that you can understand what you see when reading a Book on Programming, or on some specific Language, etc. Also, it will be easier to follow what we will be discussing later.

Top Down and Bottom Up:

I have already mentioned these concepts; let's look at them a little more. TOP DOWN is a "Logical Problem Solving" PROCESS! It is used in solving ANY Problem; whether it be repairing a piece of Electronic Equipment, writing a piece of Software, designing a Car, solving an Algebraic Problem, you name it. The procedure consists of breaking the WHOLE problem down into SMALL pieces, and breaking these down into STILL SMALLER pieces, etc. If I am going to repair a Transponder out of an Airplane, I first look at the Detected Video out of the Video Amp. If it is OK, I KNOW that the front half of the Radio, and most of the Power Supply, is operating normally (a Binary Search procedure). I have cut the Problem in half with ONE SIMPLE TEST! Solving a Programming Problem works the same way; break the overall Program down into smaller parts, and then break these down, etc., etc. It is easy, once you get used to it. Top Down PROGRAMMING, as opposed to Top Down PROBLEM SOLVING, means writing the OVERALL "Control" Program first, then different portions that it controls, etc. The easiest analogy is that of a Menu Driven Program; the Menu Program has overall Control over the WHOLE Program, and any part selected from the Menu is accomplished as called.

BOTTOM UP refers to building the actual Program from the lowest level back up to the top (FORGET IT as a Problem Solving approach). It is often called a "Building Block" process (although Building Block is NOT necessarily Bottom Up), from the similarity to building a house with "Building Blocks", or smaller pieces which make up larger ones, which are used to make up still larger ones, until the whole Program is written. As I stated, this is the procedure that is normally used with FORTH and Assembly Language Programs. Both can be programmed Top Down with the use of "Stubs" (later!), the method I personally prefer.

Procedures and Functions:

This is another area of confusion. Basically, a PROCEDURE is a method of controlling something, where a FUNCTION RETURNS A VALUE. Pascal has definite definitions and uses for each; other Languages "slide over the terms". Everything in C is a FUNCTION; BASIC has definite FUNCTIONS (SIN, COS, etc.); Assembly Language could care less. A lot depends on the specific literature you are reading as to the difference between the two. Again, I keep coming back to the "bare bones" idea that a PROCEDURE is a way of controlling program flow to accomplish some objective, while a FUNCTION CAN be the same thing, but NORMALLY returns some value.

Next month we'll continue the discussion and get into some examples. Till then;

BASIC FORUM (COCO)

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Last month we examined a specialized word processing program. The program was originally written, in some haste, to provide a formatted output to an Epson MX-80 printer. Although the program was written for the classroom, it represented a type of program which is usable in the office, laboratory, home, or wherever a columnar representation of data is needed. Unfortunately the program suffered numerous flaws. You were challenged to become a program critic, and determine ways in which the program could be improved. A number of hints were given as to how you could direct these critical efforts, and an organized review of them would be appropriate at this time.

The intention of this critical process is to develop skills of self-criticism that will eventually lead to better programs of your own. The essential procedures consist of carefully examining all facets of the program, and critically questioning the results obtained. Several possible criteria were mentioned, and we may well review them too.

(1) A program should be convenient to use. By this I mean that the user of the program should be able to complete the intended task with no undue interference from the program itself. To be succinct, virtually any reasonably intelligent person should be able to use the program with ease. Things should not happen without adequate reason.

(2) A program should be efficient. It should operate with reasonable speed, and without unnecessary pauses. It should take advantage of the capabilities of the equipment which is being used.

(3) A program should be flexible. A program should do everything that is necessary, and should be capable of easily producing common variations. It should allow these things to occur easily, without confusion on the part of the operator.

(4) A program should be understandable. There are two meanings to this. It should make sense to the operator, and the programming code should be understandable to yourself, or another programmer, at a later time.

(5) A program should be functional. Let's face some facts here. I enjoy and appreciate the efforts of some of the graphics programmers. Indeed, a graphics program may demonstrate functionality. However it is sheer distraction to be in the midst of an accounting problem and have the screen suddenly covered with a beautiful color display. The program should fit the occasion and function.

These criteria are obviously not mutually exclusive. They overlap to a considerable degree. Perhaps equally obvious is that they may be applied to any programming language, and to virtually any programming circumstance. We will refer to them frequently.

Let's examine my promised improvements to the program. Example #1 is Version 2.0 of the formatting program. As you will see, the original program has been extensively modified. My critical examination of the original program found that none of the criteria had been met. Clearly something had to be changed.

Perhaps the single worst feature of Version 1.0 was the lack of convenience and flexibility. Imagine the position of the user who is entering four columns of twenty lines. Two columns and eleven lines into the input process the typist realizes, with some horror, that line eleven has just been entered twice! What options are available? Only two? Neither of these are especially inviting. The operator may either continue, leaving in the error, or may reach for the <BREAK> key, and start over. Clearly a means of making a correction is needed.

Version 1.0 was organized around two loops, one for input, the other for output. Since I had decided to retain that relatively simple program structure, I could easily add a third loop which would allow reading through the text, and making corrections as necessary. Program lines 300 to 450 represent this effort. The delete and exchange procedure in line 400.

Version 1.0 was also lacking in efficiency and functionality. It should be clear that if one needs to have only four columns printed, 50% of the potential textual space has been lost. It is also noticeable that while even numbers of columns will present a balanced appearance on the page, uneven columnar groups will appear asymmetrically. This is simply not acceptable. As a last thought, it is highly inconvenient to keep "<ENTER>ing" for unused columns.

There is fortunately a means of simultaneously correcting both problems. Bring the entire process under control of the computer!

Version 1.0 formats through the capabilities of the printer. Printer formatting is convenient, but is neither flexible nor efficient. Can we find an algorithm in BASIC that will take full advantage of page width, and allow balanced, flexible formatting? You may want to give this some thought before you continue with my solution.

Examine lines 40 to 110 of Example #1. This group is a "menu" which presents the user's first choice. It should be self-evident. Now look at lines 560 to 610, and lines 630 to 820. Here one will find two almost identical versions of the algorithm that I finally decided to use. The use of numerous short subroutines was determined because that could correctly format both the headings, and the columns of text. The selected subroutine sets values for margin and text length. Lines 600 and 690 activate the shifts from one column to the next. Changing values of variable "K" set tab commands for the printer.

Now, if you have not typed the program into your CoCo yet, do it. Examine the BASIC coding as you type it in. Compare Version 1.0 to Version 2.0. Notice the changes in naming of variables. When possible, variable names have been related to their function in the program. Perhaps because my formal programming study was done with FORTRAN, I follow a convention that is sometimes associated with FORTRAN. Insofar as is possible I reserve the letters "J" through "N" for use with variables to pointers and counters. The original intention was that these letters were for integer use (as opposed to floating point). Some versions of BASIC allow this potential. The underlying intention is to increase speed in loops and the like. While it has no such function in the CoCo's Microsoft BASIC, it does help to organize your thinking, and to clarify program elements. I strongly recommend the practice.

As you enter, examine, and try out Version 2.0, you should continue using your critical powers. There are several improvements, "personalizations," or expansions which could be made. After you have examined and tested compare your ideas to mine which follow.

Even Version 2.0 contains minimal instructions to an operator. I have assumed that readers of BASIC FORUM will be avidly dissecting the program to see how it works, and what they can do with it. If such a program were put into the hands of a non-programmer, the instructions would be inadequate. A brief introduction could be placed ahead of the formatting menu. Perhaps a subroutine would be appropriate.

Both the input and review loops work their way through a complete column, and then progress to the next. Some may prefer to enter text and/or review text by the line rather than the column. Give this some thought. If you do not see how this might be done, examine the output loop carefully. It is so organized. After due consideration I decided that I had no need for saving text, nor loading it. Others may prefer having this potential with either disk or tape. Either could be added through subroutines located at convenient places.

I am not especially fond of the appearance of the screen during the input and review processes. It is simple, and self-explanatory, but the overall appearance could be improved. Try this out.

The review and correct loop is possibly adequate when the text will never exceed one page. It is, however, cumbersome, and not especially efficient except in terms of the amount of memory consumed. You may want to try improvements here.

There are so many conceivable changes that could be made relating to the printer that I can only suggest a few possibilities. The most obvious is perhaps modifying the program to employ a different size of type. The modification would be extensive, but could be practical to many people. Converting to a different printer is also possible. One could not examine all printers, but we can look at one possibility, Radio Shack's new DMP-200.

Richard Matsumoto, who manages Radio Shack's computer operations in the Greeley area, graciously made it possible for me to use a CoCo and the new printer to adapt the formatter. This is not really a very difficult operation. Reverting to my "brute force" method, I loaded the program into the computer, and tried printing with the DMP-200. Only a few problems were found.

The DMP-200 sets type size through an external switch. CHR\$(15) sets the underline function. Deleting line 480 corrects this problem. There are no italics available on this printer, so line 570 and the first half of line 620 may also be omitted. Since CHR\$(172) in line 620 does not produce a desirable result, that line may be changed to: 620 PRINT @-2, STRING\$(132,"="). The DMP-200 has no double printing, so lines 520 and 530 could also be omitted. There it is, an operational program for what appears to be a very good printer.

May I remind you that I welcome requests, suggestions, comments, and criticism. Next month, we'll examine some questions of clarity and understandability in programming.

EXAMPLE NO. 1

```
10 CLS:PRINTAB(7)"MX-80 FORMATTER"
20 PRINTAB(11)"V. 2.0":CLEAR 7000
30 DIM TEXT$(12,36)
40 PRINT"SEVEN POSSIBLE FORMATS:"
50 PRINT" (1) 2 COLUMNS OF 50 SPACES"
60 PRINT" (2) 3 COLUMNS OF 40 SPACES"
70 PRINT" (3) 4 COLUMNS OF 30 SPACES"
80 PRINT" (4) 5 COLUMNS OF 24 SPACES"
90 PRINT" (5) 6 COLUMNS OF 20 SPACES"
100 PRINT" (6) 7 COLUMNS OF 17 SPACES"
110 PRINT" (7) 8 COLUMNS OF 15 SPACES"
120 INPUT"ENTER THE NUMBER OF YOUR CHOICE":NUM
130 COL=NUM+1:WIDTH=INT(120/COL)
140 INPUT"HOW MANY LINES(10-56)":LIN
150 LINEINPUT"ENTER TITLE: ";TITLE$
160 PRINT"ENTER COLUMN HEADINGS."
170 FOR I=1 TO COL
180 PRINTAB(4)STRING$(WIDTH,"-")<-COL. LIMIT"
190 PRINTI;"":LINEINPUT HEAD$(I)
200 NEXT I
210 FOR I=1 TO COL:"INPUT LOOP
220 PRINT"PRINT"ENTER TEXT FOR COLUMN":I;"":POKE282,0
230 FOR J=1 TO LIN
240 PRINTAB(4)STRING$(WIDTH,"-")<-COL. LIMIT"
250 PRINTJ;"":LINEINPUT TEXT$(I,J)
260 NEXT J
270 PRINT"DO YOU WISH TO REVIEW THE":POKE282,255
280 INPUT"TEXT (Y/N)":REV$
290 IF REV$="Y" THEN 300 ELSE 460
300 FOR I=1 TO COL:POKE282,0:"REVIEW LOOP
310 PRINT:PRINTAB(5)"COLUMN 0":1:PRINT
320 FOR J=1 TO LIN
330 PRINTAB(5):"LINE 0":J
340 PRINTTEXT$(I,J)
350 PRINT"PRESS <ENTER> TO CONTINUE"
360 INPUT" OR 'C' TO CORRECT":CAS
370 IF CAS="C" THEN 380 ELSE 420
380 PRINT"ENTER YOUR REPLACEMENT."
390 INPUT" ":RP$
400 TEXT$(I,J)=""TEXT$(I,J)=RP$:RP$=""
410 PRINT:PRINT"CORRECTED-":TEXT$(I,J)
420 NEXT J
430 NEXT I:POKE282,255
440 INPUT"RE-REVIEW OR PRINT (R/P)":Z$
450 IF Z$="R" THEN 300 ELSE 460
460 INPUT"IS THE PRINTER ON (Y/N)":PR$
470 IF PR$="Y" THEN 480 ELSE 460
480 PRINT@-2,CHR$(15)
490 INPUT"HOW MANY COPIES":CP
500 PRINT"DOUBLE SPACED (Y/N)"
510 INPUT" (MAXIMUM OF 20 LINES)":SP$
520 INPUT"DOUBLE PRINTED (Y/N)":DP$
530 IF DP$="N" THEN 540 ELSE PRINT@-2,CHR$(27):"G"
540 J=LEN(TITLE$):K=INT((132-J)/2)
550 PRINT@-2,TAB(K)TITLE$
560 ON NUM GOSUB 760,770,780,790,800,810,820
570 PRINT@-2,CHR$(27):"4"
580 FOR I=1 TO COL
590 PRINT@-2,TAB(K)HEAD$(I);
600 K=K+L
610 NEXT I
620 PRINT@-2,CHR$(27):"5":PRINT@-2,STRING$(132,172)
630 FOR J=1 TO LIN:"OUTPUT LOOP
640 ON NUM GOSUB 760,770,780,790,800,810,820
650 IF SP$="N" THEN GOTO 660 ELSE PRINT@-2,""
660 FOR I=1 TO COL
670 IF I<COL THEN PRINT@-2,TAB(K)TEXT$(I,J);
680 IF I=COL THEN PRINT@-2,TAB(K)TEXT$(I,J)
690 K=K+L
700 NEXT I,J
710 M=1
720 IF M<CP THEN PRINT@-2,CHR$(12):GOTO540
730 END
740 K=6:L=60:RETURN
770 K=6:L=41:RETURN
```

```
780 K=5:L=30:RETURN
790 K=4:L=24:RETURN
800 K=4:L=20:RETURN
810 K=3:L=17:RETURN
820 K=3:L=15:RETURN
```

EXAMPLE NO. 2: Variable List

TEXT\$ The text holding array.
 NUM Format pointer
 COL Pointer to number of columns.
 WIDTH Number of spaces in a column.
 LIN Number of lines to be printed.
 TITLE\$ As indicated.
 HEAD\$ The array holding column sub-titles.
 REV\$ Selection, review or print.
 CAS Correct or not.
 RP\$ Temporarily holds the replacement.
 Z\$ Choice, review or print.
 PR\$ Printer warning.
 CP Pointer to number of copies.
 SP\$ Double spacing pointer.
 OP\$ Double print pointer.
 I & J Loop pointers
 K,L, & M Formatting variables

HELIX DM64 MEMORY SYS

In the beginning, a few years past, here in the microcomputer business, there were the bad (dynamic) and good (static) guys, of memory. Or so they said. But then there was also a lot to be learned. We learned. And the result is that the bad guys wear white hats just like the good guys. Fact is you really can't tell them apart, that is if they are fed and treated as their maker decreed.

That was really part of the problem in the early days, too many designers did not understand and use them as they should. Like any other micro-device, they have their own special way of doing things. Static was easier to tame, but cost more, also they consume more power. Dynamic was less costly but require a specific diet. Mainly consisting of proper 'refresh' pulses, dosed out at the proper time. Once the refresh pulses were fed at the right time and the other particulars attended to, well, they (ynamic) looked, for all the world, like static. Dumb old CPU does not even know the difference. Just as well, it has enough to do already, especially as processor speeds continue to climb. Now comes a new breed of memory boards that run at the normal speeds as well and efficiently as their static cousins. They actually have not changed that much. We have just learned to fed them better.

The HAZLEWOOD DM-64 dynamic memory board is one of the new breed. It is guaranteed to run at process speeds in excess of 2 million cycles per second. Which is about as fast as any of our present processors clip along.

The board is fed 'refreshed' at 2 MHZ by using a "proprietary memory controller design." A newer technology than many existing schemes. The manufacturer claims that this system has better margins at 2.5 MHZ (pretty fast) than most existing dynamic memory boards running at a 1 MHZ rate. While we can't verify this to the 'dot', we have been running an early version, of this memory board, for over two (2) years and have not once experienced a memory failure or 'bomb-out'! Our system with the DM-64 has been very dependable.

Because of the particular manner in which the DM-64 is refreshed dynamic address translators (DATs) do not appear to affect the read access timing as in the case of some static boards.

Configuration

The DM-64 is fully configurable by 4KB segments by on-board switches, and may be set for 64KB segments in a 1 megabyte (1,000,000) address range. Extended address lines may also be ignored by switch settings, allowing operation on systems where these lines are dedicated to other functions. Setup is made simple by these selection switches and allow you to configure to your system with a minimum of effort, skill and time. Adequate charts are furnished for configuration instructions. The DM-64 can be used in Standard S50 Bus systems as well as the newer 50/64 bus systems (HELIX).

Warranty and Construction

The board comes completely wired and tested to 2.5 MHz operation. The warranty is one (1) year from date of purchase, by the USER. Some other boards are not always covered from the date of end-user installation. In this case they are, and that is an important consideration from those buying from a dealer who may have had the board in stock for a few weeks or even months. Just hang on to your sales ticket.

Experience

We have been running a DM-64 for over two years as I stated earlier. Our particular board has not had the regulator heat sink modifications. We have not experienced any memory problems. Later production boards have had few modifications from the original boards. The heat sinks on the regulators was one of the few updates and did eliminate some problems with the very first batch of boards. We are aware of users who had been running the DM-64 for over the one year warranty period, then began to experience or suspect that they were having memory problems. In every case that we are aware of, Hazlewood repaired or updated them at NO cost to the user, necessary or not. We consider that service.

Documentation

The instructions are adequate. The manual consist of a couple of sheets that basically cover what I have written above. Also is a full page configuration chart. The diagrams are complete and include a component layout chart. Despite the rather short manual, it is more than adequate for installation. The only thing missing is a 'blow by blow' description of what all happens (signal wise) on the board. While not necessary for installation, fuller signal descriptions do come in handy for a few of us 'do it yourselfers'. However, I guess we are a dying breed as most newer users are more concerned with out-of-the-box operation. Also if the time comes they will most likely want to ship it back to the manufacturer, or dealer for service. Actually that is a much better way. I have seen many good boards ruined by 'expert repairers'. Of course if yours works as well as ours has, there is no actual need for a lot of technical data.

The board is quality glass epoxy, double sided and silk-screened, with gold edge connectors. All lcs are socketed. It is installed on the S50 bus and fits all popular cabinets.

We have recently installed the big-brother 256KB Hazlewood memory board in one of our 'use every day' computers and will have a report on it at a later date. It also is guaranteed to 2.5 MHz.

For additional information contact:

HAZLEWOOD COMPUTER SYSTEMS
907 E. Terra
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(314) 281-1055

WESTCHESTER UTILITIES

Westchester Applied Business Systems FLEX9 Utilities

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Introduction

There are many FLEX9 operating system utilities available for purchase from a variety of sources these days. I purchased several such utility programs from Westchester Applied Business Systems, P.O. Box 187, Briarcliff Manor, New York 10510. I have been using the utilities for over six months on an almost daily basis. The utilities I purchased include:

- | | |
|-------------|-----------------------------------|
| 1) LISTDS | displays the disk directory |
| 2) MCOPI | a single-drive file copy program |
| 3) PMAP | displays a binary file load map |
| 4) DMAP | displays a file sector map |
| 5) DUMP | displays memory dump in HEX/ASCII |
| 6) DOSK | displays disk sector in HEX/ASCII |
| 7) ZAP | disk sector binary editor |
| 8) LOGO | prints enlarged text |
| 9) RESIZE | adjusts text file line width |
| 10) TYPESET | justifies text file line margins |
| 11) LISTM | columnizes a text file |

I will review each utility program in turn, but first I would like to touch on several important areas of any software purchase.

Documentation

The documentation is quite good. Each utility has a single sheet of documentation which is printed so as to easily integrate with the TSC FLEX9 user's manual. Each page gives a brief description and includes a definition of the general command syntax and also includes several examples.

Support

When I received my disk and documentation I began to use my new software quite heavily. I discovered that a page of documentation for one of the utilities was in error. I also uncovered a "bug" in one utility and discovered that yet another utility didn't have all the features I considered essential to that utility program. While it is reasonable to expect correction of the first two problems, it is stretching "support" just a little on the third item. I should also point out that these utilities cost less than five dollars each, and though good product support should be provided by a vendor for all of his products, I was very surprised to receive outstanding service on all three of my problems.

The proprietor, Mr. Bill Adams, was pleasant, courteous, and prompt in his service. He sent me an amended documentation page along with the corrected bug and my suggested addition to the one utility program. Even though there were these initial problems, the excellent service more than compensated for them. The importance of good vendor support just cannot be overemphasized. Now on to each of the utility programs!

LISTDS

This utility displays the disk directory in three columns and all three columns fit within an eighty character display. The directory display consists of filename, starting and ending tracks and sectors, file size, and file creation date.

MCOPI

This is a single-drive file copy utility program. You can copy disk to disk with only one disk drive. MCOPI is loaded and then prompts for you to insert first the source disk and then the target disk so that MCOPI does not have to reside on either the file source or target disk. It copies a single file at one time as long as the source program does not exceed available memory size.

PMAP

This utility displays the memory load map and the file transfer address for a binary file. Each disk load record is displayed with its starting and ending memory load addresses. The transfer address of the binary file is also displayed.


```

F4CC 57 41 42 45
F4D0 20 52 41 4E
F4D4 47 45 20 46
F4D8 4F 52 20 42
F4DC 4F 41 52 44
F4E0 20 30 20 49
F4E4 53 20 30 2D
F4E8 44 46 37 46
F4EC 04

```

```

* FCB #04 END OF STRING POINTER
* GO TO SBUG-E MONITOR MEMORY TEST ROUTINE
*
F4ED 8D F57E LFOR LFCR
F4F0 8E F4FA LDX @PDATA1 ISSUE PROMPT
F4F3 8D 9F F90C JSR SBUGG GO TO SBUG-E MEMORY TEST
F4F7 7E F9EF
*
F4FA 45 4E 54 45 ADDRMSG FCC /ENTER MEMORY TEST 1 START ADDR-END ADDR

```

```

F4FE 52 20 40 45
F502 40 4F 52 59
F506 20 54 45 53
F50A 54 20 3A 20
F50E 53 54 41 52
F512 54 20 41 44
F516 44 52 20 45
F51A 4E 44 20 41
F51E 44 44 52 20
F522 04
F525 45 4E 54 45 HTRMSG FCC /ENTER 0 TO TEST 0-40K 1 ENTER 1 TO TEST

```

40K-56K 1/

```

F527 52 20 30 20
F52B 54 4F 20 54
F52F 45 53 54 20
F533 30 20 34 30
F537 48 20 30 20
F53B 45 4E 54 45
F53F 52 20 31 20
F543 54 4F 20 54
F547 45 53 54 20
F54B 34 30 48 20
F54F 35 36 48 20
F553 3A
F554 04
F555 45 4E 54 45 BMSG FCC /ENTER EXTENDED MEMORY BOARD 0 TO TEST 1

```

```

F559 52 20 45 50
F55D 54 45 4E 44
F561 45 44 20 40
F565 45 40 4F 52
F569 59 20 42 4F
F56D 41 52 44 20
F571 23 20 54 4F
F575 20 54 45 53
F579 54 20 3A 20
F57D 04

```

```

* FCB #04 END OF STRING POINTER
* LINE FEED OR ROUTINE
*
F57E 86 0A LFOR LDX @PDATA1 LINE FEED
F584 86 00 JSR @PDATA1 OR
F586 8D 9F F80A JSR @PDATA1
F58A 39
*
* RPN LOCATIONS
F58B 8D 00 EDU *
F58D 04 END RESERVE RPN MEMORY WORD

```

0 ERROR(S) DETECTED

SYMBOL TABLE:

```

ADDRMSG F4FA BMSG F555 DATADR FFF0 DOSRAM F44D DRAMTS DF00
PRIVATE DPDF F58B INCHE F806 LFOR F57E LOOP1 F42E
LOOP2 F442 MSG F472 HTRMSG F523 OUTCH F80A PDATA F80C
Q F4ED 00 F4B9 BMSG F4C8 SBUGG F9EF START F400

```

SORTED DIRECTORY

by

F. James Rohlf
Dept. of Ecology and Evolution
State Univ. of New York
Stony Brook, NY 11794

The following program (written in Lucidata Pascal 3.9) prints on a printer and/or displays on a terminal a 3 column directory of the files on the disk in drive 1. The entries are sorted alphabetically -- which greatly helps in finding a misplaced file. The multicolumn display makes it possible to have the information from many files on the screen at once. The sector addresses are not displayed since they are seldom of interest.

The printed report makes use of the condensed print option found on most dot matrix printers. This makes it possible for the report to be cut out and taped to the floppy disk envelop. I have found this greatly helps in organizing ones disks. Small changes may be needed in two of the procedures if a printer other than a PRISM80 is used (one has to substitute the appropriate printer control codes).

A sample printout is furnished below.

Send to printer? (y/n)

Directory (4/14/83)
Disk: FJR-WORK, # 4, Created: 4/14/83

File	Size	Created	File	Size	Created
COPY .CMD	4	7/19/82	MISC .TXT	72	2/18/83
OPTIMICRO.TXT	14	2/1/83	NEWSRS .TXT	53	2/19/83
JUNK .OUT	0	4/14/83	PDIR .CMD	31	3/4/83
LADUCE .TXT	50	2/19/83	PDIR .PAS	18	3/4/83
MICRO68 .TXT	5	4/14/83	PDIRNEW .BAK	20	4/12/83
			PDIRNEW .PAS	24	4/14/83
			PDIRNEW .BIN	0	4/12/83
			TERMINAL .ASM	38	12/10/82
			XREF .PAS	25	4/14/83

Files: 14, Sectors: 325, Free: 289
Do another disk? (y/n)

```
program pdir(input,output,lpt);
```

```
{ print or just display sorted disk directory
  Gives file name & extension, size, and date.
  Up to 57 entries can be display on a CRT at
  once.
```

```
Printed output is < 5" wide so it can be cutout
and taped to (or inserted in) the floppy disk
envelope.
```

The program assumes LUCIDATA Pascal, FLEX19 (but only a few address constants need be changed for FLEX), and a PRISM80 printer (procedures norprmt and condprt will need to be changed for other printers). Note: LUCIDATA Pascal assumes that the PRINT.SYS print drivers have already been loaded (if you have not yet printed anything you could type GET PRINT.SYS).

F. James Rohlf, 1983, Dept. of Ecology & Evolution
State Univ. of New York, Stony Brook, NY 11794

```
const
```

```
maxlen = 120; { maximum no. of directory entries }
null = chr(0);
minrows = 5; { minimum no. rows in directory table }
```

```
type
```

```
{nameT = array[1..8] of char;
textT = array[1..3] of char;
```

```
var
```

```
fcbl : integer; { address of the FCB }
```

```

fcode : byte; { FLEX file control block }
errstat : byte; { many of these entries are }
actstat : byte; { not actually needed by this }
drive : byte; { program but are furnished }
fname1 : byte; { for reference }
fname : array[1..7] of char;
text : textT;
fattribute : byte;
byte16 : byte;
startadd,endadd : integer;
fsize : integer;
fsecmap : byte;
byte24 : byte;
month,day,year : byte;
fcblptr : integer;
curpos,currecno : integer;
dataindex,randindex : byte;
namewkbuf : array[1..8] of char;
track,sector : byte;
startindx : byte;
firstdeldir : array[1..3] of byte;
scratch : array[1..8] of char;
spaceflag : byte;
secbuffer : array[1..256] of byte; { end of FCB }

```

```

lpt : file of char;
ans : char;
i,j,k,kk,nrows,diskno : integer;
nfree,nsec : integer;
print : boolean;

```

```

tabname : array[1..maxlen] of alfa;
tabext : array[1..maxlen] of textT;
tabsize : array[1..maxlen] of integer;
tabdate : array[1..maxlen,1..3] of byte;
tabptr : array[1..maxlen] of integer;
tablen : integer;

```



```

($ADDRESS = $CCOE)
mm,dd,yy : byte;      ( today's date from FLEX )
($STACK)

( ** some procedures that may have to be modified ** )

procedure fmscall(fcb:integer); ( ** assumes FLEX9 )
external $D406;
function address (var fcode :byte): integer;
external $174;

procedure normprt; ( select normal print 6 lines/inch,
12 chars/inch )
begin ( ** assumes PRISM 80 printer )
writeln(lpt,chr(30),chr(27),'B,8,$');
end;

procedure condprt; ( select print 8 lines/inch,
16.8 chars/inch condensed mode )
begin ( ** assumes PRISM 80 printer )
write(lpt,chr(31),chr(27),'B,6,$');
end;

( ** end of procedures that may need to be modified ** )

PROCEDURE SORT ( n : INTEGER ) ; { Simple bubble sort }
VAR
i,temp : integer;      ( This could be replaced )
done : boolean;        ( if you have many files. )

BEGIN
REPEAT ( sort pointers to files )
done := true;
FOR i:= 1 to n-1 DO
IF tabname[tabptr[i]] > tabname[tabptr[i+1]] then
begin
temp:=tabptr[i];      ( out or order, swap )
tabptr[i]:=tabptr[i+1];
tabptr[i+1]:=temp;
done := false
END;
UNTIL done
END;

procedure line;      ( display a line --- )
var
i,j : integer;

begin
for i:= 1 to 3 do begin
for j:= 1 to 25 do write(lpt,'-');
if i<3 then if print then write(lpt,' ')
else write(lpt,' ');
end;
writeln(lpt);
end;

begin ( MAIN PROGRAM )
writeln;

REPEAT
write('Send to printer? (y/n)'); ( default is "NO" )
readln(ans); writeln;
if (ans='Y') or (ans='y') then begin
assign(lpt,'P:'); ( printer )
print := true;
end else begin
assign(lpt,'C:'); ( output to CRT )
print := false;
end;
rewrite(lpt);

( The following calls FLEX system info )

tablen := 0;
fcb := address(fcode);
drive := 1;      ( assumes drive #1 only,
this could be changed of course )

fcode := 16;      ( open system info record )
fmscall(fcb);

```

```

fcode := 7;      ( get system info record )
fmscall(fcb);

if print then normprt; ( normal print mode )

writeln(lpt,'          Directory (',
mm:2,'/',dd:2,'/',yy:2,') ');
diskno := fatattribute+256+byte16;
write(lpt,' Disk: ',chr(fname1),fname,' #',diskno:4);
write(lpt,' Created: ',fsecoap:2,'/',byte24:2,'/',month:2);
writeln(lpt);

if print then condprt; ( condensed print mode )

nfree := fsize;
nsec := 0;
line;

fcode := 6;      ( open directory )
fmscall(fcb);
while errstat<>8 do begin ( get file names from directory
( get info record )
fcode := 7;
errstat := 0;
fmscall(fcb);
if (fname1<0) and (fname1<127) then begin
tablen := tablen+1; tabptr[tablen] := tablen;
tabname[tablen] := ' ';
tabname[tablen,1] := chr(fname1); ( valid file name? )
for i:= 1 to 7 do
if fname[i]<>null then
tabname[tablen,i+1] := fname[i];
tabext[tablen] := ' ';
for i:= 1 to 3 do ( file extension )
if fext[i]<>null then tabext[tablen,i] := fext[i];
tabsize[tablen] := fsize;
nsec := nsec+fsize;
tabdate[tablen,1] := month; ( file creation date )
tabdate[tablen,2] := day;
tabdate[tablen,3] := year;

end;
end;

sort(tablen); ( sort pointers to file names )

( output directory in 3 columns if possible )

nrows := (tablen+2) div 3;
if nrows<minrows then nrows := minrows;
if nrows>tablen then nrows := tablen;

for i:= 1 to nrows do begin
kk := i;
for k:= 1 to 3 do begin
if kk<= tablen then begin
j := tabptr[kk];
write(lpt,tabname[j]:8,'.',tabext[j]:3,' ',
tabsize[j]:3,' ',tabdate[j,1]:2,'/',
tabdate[j,2]:2,'/',tabdate[j,3]:2);
end;
if k<3 then
if print then write(lpt,' ')
else write(lpt,' ');
kk := kk+nrows;
end;
writeln(lpt);
end;
line;
if print then write(lpt,chr(30),chr(27),'B,7,$');
write(lpt,'Files:',tablen:3,' Sectors:',nsec:4,
Free:',nfree:4);
writeln(lpt);
if print then page(lpt);
write('Do another disk? (y/n)'); ( default is NO )
readln(ans); writeln;
UNTIL (ans<>'Y') and (ans<>'y');
end.

```

BIT Bucket

Wilson N. Killebrew, Jr.
File: RUMACROS.A41

Feb 2, 1983 Page 2

Intel's FLEX89 assembler is the first macro assembler I have used in over eighteen years of computer experience. That must seem incredible to those who work in data processing. Only one of the machines I have worked with is even still in production, the Honeywell TDC4100. I doubt that most of you have even heard of it. It is descended from the GE 4020 series and their ancestor, the GE 412 which dates from the late 50's. Unlike most of the early machines, these are relatively addressed. They use a twenty-four bit word, probably unique in the industry.

I am new to the micro field, having been involved only just over a year. I was very much interested in the Altair when it first appeared when I examined the Intel instruction set. I put it aside in total disgust. The Intel instruction set is awful, but their mnemonics are even worse. If that is possible! Intel improved the mnemonics, but I can never forgive them for perpetuating and popularizing such a truly horrible instruction set. This is likely preaching to the saved, but even though the 6803 has some features that could some improvement, it is a very good programming machine. The 6809 instruction set is why, after all these years, I finally bought my own computer.

No doubt there are a lot of people who are becoming interested in assembly language programming but who, like me, haven't learned to use the power of macros. A continuing exchange of programming tools would be welcome. I could certainly use some good ideas to improve my own productivity.

There appears to be a fair number of 6809 owners who do not own assemblers with macro and/or conditional assembly capabilities. If you are one of these and are serious about writing in assembly language, you should obtain a good macro assembler with conditional assembly features. It will be well worth the money. Many feel they can't afford a lot of software, having mortgaged the farm just to buy some hardware. If you have Smoke Signal's assembler, catches have been published to implement macros. If you have something else, I suppose the only alternative is to write your own macro assembler or figure out how to patch what you have. Good luck! A group project would be better: too many cooks may spoil the pudding, and committees may build camels instead of horses, but a variety of views can lead to better software. HOWARD HARRISON has proposed establishing a 6809 software library. It is a good idea. Where do we start?

6809 PROGRAMMING TOOLS
RAC905 AND CONDITIONAL ASSEMBLY

Wilson N. Killebrew, Jr.

M. L. Harkness' remarks in 68 Micro Journal and RUMACROS documentation set me to thinking about how to implement "real counters" for FLEX macros. I had been wrestling with the problem of ensuring unique labels created by repeated use of macros having internal labels.

My problem arose originally in converting macros from a Z80 macro assembler, VENUS, to FLEX89. I wanted to have the FLEX89 macros as powerful as possible as the 6809 code would be as good as I knew how to make it. Some of you may have seen the project in Dr. Dobbs' Journal. The macros and a program written in them implement TINCOMP, a Precompiler for a compiler compiler.

Recently the flash of inspiration came. I'll not be disappointed if I'm telling some of you something you already know if this will encourage others to help make this a continuing column on programming tools. Someone always seems to have a good trick that is not well known. And I don't mean the kind that make for obscure programming entries. Good ones.

The solution: use conditional assembly in a macro. It is so simple I don't know why I didn't think of it sooner. To have unique labels for repeated macro invocations, such as:

```
ABCD MACRO ARGUMENT
    MAKELABEL STATEMENT, REMARKS
ENDM
```

the second macro, MAKELABEL, can supply the needed counter function and the actual labels. It can even be shared by any number of macros etc. The form as shown, creating a complete, labelled assembler statement looks quite clumsy, so one may prefer to use labelled equates. My counter macro looks like:

```
COUNT0 SET 0           Initialize counter
MAKELABEL MACRO 'statement', 'remarks'
    IF COUNT0 = 0
        '81', '82'
    EXISTM
    IF COUNT0 = 1
        'A1', 'A2'
    EXISTM
    1
    IF COUNT0 = 99
        '81', '82'
    EXISTM
    EQU Counter & value out of limits
    SET COUNT0 0
ENDM
```

ACORN
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ACS designs and manufactures the ACORN CONQUER SYSTEM, a powerful, yet compact system for the engineer/experimentist, as well as the small businessman.



- STACKABLE COMPUTER MODULES, 3) a 12 a 15 inch.
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GIMIX 1331 WEST 37th PLACE • CHICAGO, ILLINOIS 60608 • (312) 67-0540 • Telex 810221-408

Apr 27, 1983

Don Williams
68 Micro Journal
P.O. Box 849
1500 Connecticut
Gibson, IL 61734

Dear Don:

6809 versions of FLEX are produced by Gimix under license from USC for use only with Gimix controllers. Your readers should be careful of anyone offering controllers compatible with Gimix versions of FLEX. This does not mean that we will sell you a legitimate copy of FLEX. We can not and will not.

We have not licensed anyone to use or sell our proprietary abilities, routines, or software other than for use with our FLEX and hardware.

We are of course aware that our versions of FLEX have become more popular than our sales of controllers would indicate. It is 100% so compatible with users who sell only controllers and do not have the costs of licensing, debugging, and supporting software for the other controller boards, but compare we will. Therefore, immediately, we are lowering the price of Gimix FLEX to 138.00 when ordered with our 6809 controller.

We are also offering to our readers a Gimix system specially priced at 12595.00. This system includes: 805 CPU, 219 Kbit CLEVER, 56K or 1024K RAM, a 5 1/4 inch disk drive and software. 246 disk controller, LMBUS, and FLEX. Drives are available, but not included in this price. Our 228 double density controller may be substituted for 3100 more.

Very truly yours,
GIMIX, Inc.

Richard Don
Richard Don, V.P.

83/jn

MIDDLESEX POLYTECHNIC
PSYCHOLOGY LABORATORIES
ENFIELD, MIDDLESEX
EN3 4SF
ENGLAND
Telephone 01 804 8131

I heartily agree with Ronald Anderson's Editorial in the October 1982 issue of 68 Micro Journal.

I have faced the problem of a mixture of SWTPC 1MHz and 2MHz CPU boards. Any user who has the same problems should

consider the following, before tearing their hair out.

1) Have you tried to run a 2MHz CPU board with other boards containing 1MHz components?

I found that the Eproms used in our system work when installed on a 2MHz CPU board, although created on a 1MHz computer.

2) What is the effective frequency of data transfer through the ports?

On our system (see APRIL '82 issue of 68 Micro Journal) the 2MHz computers, all SWTPC S-09's, communicate through old style SWT MP-LA parallel interfaces, with 6820's installed, to a 1MHz central computer.

If 1MHz boards disappear from the market many users, especially home users will find the cost of upgrading too high. The alternatives are to give up using 68XX microcomputers or to go into do it yourself interfacing with 6809 home colour computers.

As Ron says in his editorial the 68000 is a great processor. However, the cost in the UK (and probably the US) is only worthwhile if you can go all the way. This means hard disks with a suitable high grade (expensive) printer and tape or double sided disk drives as backup. Is the 68000 computer a microcomputer?

Like many others we are committed to the 6809, on the SS-50 bus, and cannot change if for no other reason than finance.

Lastly, I must add that all opinions expressed in this letter are personal.

Yours Sincerely,

Brian Roberts

BRIAN ROBERTS

GIMIX INC. 1337 WEST 37th PLACE • CHICAGO, ILLINOIS 60609 • (312) 827-5610 • TWX 910-221-4055

APRIL 25, 1983

Larry Williams
68 Micro Journal
P.O. Box 849
5300 Cassandra Sulphur
Alison, Va 27143

Dear Larry:

Enclosed is our new ad.

I call your attention to our new 6809 systems featuring our new GMS 111 CPU, GMS 111 OS-9, and Intelligent I/O Processor card.

This system, with a 19MB Winchester drive, is priced at only 70 cents more than the system in our prior ad. It offers in addition to the new CPU and I/O cards, 64K word 8080/8085 BASIC OS and OS9.

Please note that in all our OS-9 systems we are now including the OS-9 Debugger, Editor, assembler, BASIC OS and OS9 under license from MICROSOURCE.

We expect all GIMIX dealers to be able to give full support and service to our users. Any user who does not believe he is getting this support from any of our dealers should contact Bob Phillips or me directly. We assure you that we will take immediate measures. We intend that the quality of service, at GIMIX and its dealers, be equal to the quality of our equipment.

Very truly yours,
GIMIX, Inc.

By *Richard Don*
Richard Don, V.P.

'68' Micro Journal

PRESS RELEASE

INTELLICOM

(An INTELLIGENT COMMUNICATIONS Program)

INTELLICOM provides you with the capability of very intelligent computer to computer communications from both a terminal emulation and file transfer standpoint. INTELLICOM supports several file transfer protocols that facilitate the transfer of both binary and ASCII data and files. Since INTELLICOM is menu driven, it is a breeze to use and understand.

With INTELLICOM you will be able to communicate with the various data and timesharing services such as the Source and CompuServe. Additionally, since INTELLICOM supports the protocol used by virtually all remote CP/M systems around the world, all users can immediately begin to take advantage of the wealth of public domain software available on these systems. Current, or potential, users of CompuServe can transmit and receive both binary and ASCII data with full error detection and recovery.

INTELLICOM will operate in the following modes:

- Terminal (full duplex)
- Terminal with local echo (half duplex)
- Terminal with data capture (either half or full duplex)
- CompuServe Executive

INTELLICOM provides the following modes of file transfer:

- Buffered ASCII capture with Xon/Xoff handshaking
- Buffered RCPM block checksum using Christensen protocol
- Buffered CompuServe "A" block checksum
- Buffered CompuServe ASCII capture (PTP/PTP protocol)
- Buffered Standard Intel Hex formatting

The checksum protocols allow for the verification of data blocks transferred (assuming appropriate support on the host end). This feature will be of great value in those applications where data integrity is paramount. INTELLICOM's documentation includes a detailed description of all protocols used along with machine readable examples of host pseudo code that will greatly ease the task of implementing support for INTELLICOM on any existing in-house mainframe (IBM, Dec, Wang...).

Customized (load and go) versions of INTELLICOM are currently available for the: IBM Personal Computer (PCDOS 1.1), NorthStar Horizon, NorthStar Advantage, Digital VTI80/181, OS9 and Flex 9. The NorthStar and Dec machines require CP/M 2.2. All systems must be equipped with a minimum of 50Kb of memory, at least one disk drive, and a cable or direct connection to another computer. Other considerations are subject to availability and a slight surcharge. INTELLICOM can be purchased from JCT ASSOCIATES. Please see ad 68 Micro Journal.

Jim Bellomo

Tri-Star Corporation

January 3, 1983

Mr. Don Williams, Sr.
Publisher
"68" Micro Journal
3018 Hamill Road
P. O. Box 849
Hanson, TN 37343

Dear Don:

I am sorry I am late sending this information as I told you I would do when I talked to you on the phone some weeks ago. However, I have been extremely busy and it just popped into mind this morning.

This is a simplified method of copying files from the hard disk to floppy for back up where the entire disk needs to be copied using double sided/double density formatted floppies.

Print a listing of the files on the hard disk and with an adding machine, starting with the #1 file, total the file record lengths until reaching approximately 3300 sectors. Draw a line across the file list at this point. Starting with the next file, again total approximately 3300 sectors, draw a line. Continue this process until the hard disk has been broken up into approximately 3300 sectors.

Insert the first floppy and copy hard disk to floppy. As the copying proceeds, watch for the file just prior to the first line. When it starts to copy this file, tap the tab or escape key once and wait until the file is complete. At this point, the system will shut down. Remove the floppy and insert another floppy. Tap the tab or escape key once and the process will continue. Continue this process until all the data has been copied off.

The disk you have copied this way will not operate until you run FORTHOS on the floppy. At this point you have a complete copy of all the working files. Hope this helps somebody.

Very truly yours,

Robert G. Van Houten
Robert G. Van Houten

RGVH/sj

29

A couple of items from Bob Rosen of SPECTRUM PROJECTS and "Bulletin Board" fame! The first is some information on "the New Color Computer ROMs". It is a list of the Addresses that are different from the previous Versions. NOTE especially the comments on the new Disk ROM; that will cause some problems. Also, here is a short summary of the necessary changes for updating Version -D and -E Boards to 64K RAM; there is a lot of confusion about the mods. Most of the suppliers of the Mods have a good set of Instructions: including DATA-COMP. I added the 33 ohm resistor note; I have seen three MC6883 Chips blown when the Mod was accomplished without this resistor.

--- RLN ---

SPECTRUM PROJECTS
93-15 86 DRIVE
WOODHAVEN, N.Y. 11421

Voice Line (212) 441-2807
Data Line (212) 441-3755/3766

64K "D" Board Upgrade

1. Remove capacitors C61, C31, C64, C35, C67, C45, C70, and C48.
2. Move the Jumper at the right of U10 to the 16K position and remove the Jumper plug between U8 and U4.
3. Make the following cuts:
+5V to pin 9 of the ram
+12V to pin 8 of the ram
-5V to pin 1 of the ram
4. Add the following jumpers:
+5V to the ram pin 1
+5V to the ram pin 8
Pin 35 of U10 to pin 9 of the ram. *USE 33-Ω RESISTOR*
Pin 12 of U4 to pin 16 of U8.
5. Bend pins 4, 5, and 6 of U29 and pin 5 of U11 up in the air.
6. Connect pin 6 of U29 to pin 8 of U29.
7. Connect pin 4 of U29 to pin 5 of U11.
8. Connect pin 5 of U29 to TP1.
9. Install 64K chips in sockets U20-U27.

64K "E" Board Upgrade

1. Remove capacitors C61, C31, C64, C35, C67, C45, C70 and C48.
2. Set the jumper between U8 and U4 to the 32K position.
3. Set the jumper plug located just below C44 to the 16K/32K position.
4. Set each of the three jumper plugs located just above the keyboard connector to the 32K position.
5. Solder the two staking pins next to U29 to be in the LOW position.
6. Solder the two staking pins to the left of C44 together.
7. Bend pins 4, 5, and 6 of U29 and pin 5 of U11 up in the air.
8. Connect pin 6 of U29 to pin 8 of U29.
9. Connect pin 4 of U29 to pin 5 of U11.
10. Connect pin 5 of U29 to TP1.
11. Install 64K chips in sockets U20-U27.

COCO BASIC ROM 1.2 ADDRESSING

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A9DC A9DD A9DE A9DF A9E0 A9E1 A9E2 A9E3 A9E4 A9E5 A9E6 A9E7 A9E8 A9E9 A9EA A9EB
A9EC A9ED A9EE A9EF A9F0 A9F1 A9F2 A9F3 A9F4 A9F5 A9F6 A9F7 A9F8 A9F9 A9FA A9FB
A9FC A9FD A9FE A9FF A9A0 A9A1 A9A2 A9A3 A9A4 A9A5 A9A6 A9A7 A9A8 A9A9 A9AA A9AB
A9AC A9AD A9AE A9AF A9B0 A9B1 A9B2 A9B3 A9B4 A9B5 A9B6 A9B7 A9B8 A9B9 A9BA A9BB
A9BC A9BD A9BE A9BF A9C0 A9C1 A9C2 A9C3 A9C4 A9C5 A9C6 A9C7 A9C8 A9C9 A9CA A9CB
A9CC A9CD A9CE A9CF A9D0 A9D1 A9D2 A9D3 A9D4 A9D5 A9D6 A9D7 A9D8 A9D9 A9DA A9DB
A9DC A9DD A9DE A9DF A9E0 A9E1 A9E2 A9E3 A9E4 A9E5 A9E6 A9E7 A9E8 A9E9 A9EA A9EB
A9EC A9ED A9EE A9EF A9F0 A9F1 A9F2 A9F3 A9F4 A9F5 A9F6 A9F7 A9F8 A9F9 A9FA A9FB
A9FC A9FD A9FE A9FF A9A0 A9A1 A9A2 A9A3 A9A4 A9A5 A9A6 A9A7 A9A8 A9A9 A9AA A9AB
A9AC A9AD A9AE A9AF A9B0 A9B1 A9B2 A9B3 A9B4 A9B5 A9B6 A9B7 A9B8 A9B9 A9BA A9BB
A9BC A9BD A9BE A9BF A9C0 A9C1 A9C2 A9C3 A9C4 A9C5 A9C6 A9C7 A9C8 A9C9 A9CA A9CB
A9CC A9CD A9CE A9CF A9D0 A9D1 A9D2 A9D3 A9D4 A9D5 A9D6 A9D7 A9D8 A9D9 A9DA A9DB
A9DC A9DD A9DE A9DF A9E0 A9E1 A9E2 A9E3 A9E4 A9E5 A9E6 A9E7 A9E8 A9E9 A9EA A9EB
A9EC A9ED A9EE A9EF A9F0 A9F1 A9F2 A9F3 A9F4 A9F5 A9F6 A9F7 A9F8 A9F9 A9FA A9FB
A9FC A9FD A9FE A9FF A9A0 A9A1 A9A2 A9A3 A9A4 A9A5 A9A6 A9A7 A9A8 A9A9 A9AA A9AB
A9AC A9AD A9AE A9AF A9B0 A9B1 A9B2 A9B3 A9B4 A9B5 A9B6 A9B7 A9B8 A9B9 A9BA A9BB
A9BC A9BD A9BE A9BF A9C0 A9C1 A9C2 A9C3 A9C4 A9C5 A9C6 A9C7 A9C8 A9C9 A9CA A9CB
A9CC A9CD A9CE A9CF A9D0 A9D1 A9D2 A9D3 A9D4 A9D5 A9D6 A9D7 A9D8 A9D9 A9DA A9DB
A9DC A9DD A9DE A9DF A9E0 A9E1 A9E2 A9E3 A9E4 A9E5 A9E6 A9E7 A9E8 A9E9 A9EA A9EB
A9EC A9ED A9EE A9EF A9F0 A9F1 A9F2 A9F3 A9F4 A9F5 A9F6 A9F7 A9F8 A9F9 A9FA A9FB
A9FC A9FD A9FE A9FF A9A0 A9A1 A9A2 A9A3 A9A4 A9A5 A9A6 A9A7 A9A8 A9A9 A9AA A9AB
A9AC A9AD A9AE A9AF A9B0 A9B1 A9B2 A9B3 A9B4 A9B5 A9B6 A9B7 A9B8 A9B9 A9BA A9BB
A9BC A9BD A9BE A9BF A9C0 A9C1 A9C2 A9C3 A9C4 A9C5 A9C6 A9C7 A9C8 A9C9 A9CA A9CB
A9CC A9CD A9CE A9CF A9D0 A9D1 A9D2 A9D3 A9D4 A9D5 A9D6 A9D7 A9D8 A9D9 A9DA A9DB
A9DC A9DD A9DE A9DF A9E0 A9E1 A9E2 A9E3 A9E4 A9E5 A9E6 A9E7 A9E8 A9E9 A9EA A9EB
A9EC A9ED A9EE A9EF A9F0 A9F1 A9F2 A9F3 A9F4 A9F5 A9F6 A9F7 A9F8 A9F9 A9FA A9FB
A9FC A9FD A9FE A9FF
```


Dear Don:

I would like to give my impressions as a user of a new product recently introduced by CREATIVE MICRO SYSTEMS. The product, simply called the 9687 TABLETOP MICROCOMPUTER, features a hard disk, with 20 megabytes of formatted storage, a 08,080 3 1/2 inch floppy, and a 14 slot EXPANDABLE motherboard. The power supply is of the switchmode design.

The microprocessor board, designated the 6819 runs at 2 mhz. The on board features include a real time clock, 4800 timer, two serial ports, and three PPI's. A 9678 host adapter (for the hard disk) and a 9628 4K CMOS ROM module populated to 360, fill two more slots leaving eleven open for expansion or specialization. Breakout boards provide the buffering between the CPU and the peripherals. The unit in its minimum configuration, supports two serial terminals, and two parallel printers. A module can be used in place of the second terminal if so desired. The rear panel has cutouts to accommodate eight additional serial devices, 2 additional drives and an EPROM programming head.

With memory management, and OS-9, level two, expansion to 1 mbyte of static memory is possible. This unit was delivered with OS-9 installed. Software included OS-9, level 1, Assembler, Editor, Debugger, and Basic80. Also available for installation by the factory is the full complement of Microware products, the Stringdash Word Processor, Spelling Checker, and Mailmerge. Additionally, a Data Base Management program, and other languages can be installed at time of purchase or at a later date.

My principle use for the system is Word Processing. I am fully satisfied with the operation of Stringdash under OS-9. It operates exactly as it does in my File System, with only minor variations on the command line and a much faster response. I have had only a smattering of experience with Basic80, but having transferred a program from Basic to Basic80, I find that after adjusting for CPU speed, the transferred program runs 2 1/2 times faster.

The transition from File to OS-9 was about as can be expected. Software manuals 'assume' that you are proficient in the use of every program except the one you are using, so references to some interacting procedures could be made clearer. With use, the dog disorients and the reasoning falls into place. Having used File for several years, the reasoning behind a multi-level directory became clear when I realized that it would be virtually impossible to locate a program on a 28 mbyte single level directory.

While the manuals for the software leave something to be desired, CREATIVE MICRO SYSTEMS has accomplished an outstanding job in the writing of their product specification. The specifications even include the dates for modification availability of software and hardware to save the CME 9687 TABLETOP MICROCOMPUTER even more versatile.

Specifying of versatility, here is a partial list of the hardware support available for the CME TABLETOP:

- 640K 128K Static ROM Module
- also 64K, 32K ROM modules
- 9639 Memory Management Processor Module
- 9622-S Serial/Parallel I/O Module

Additionally, the CME 9687 CRT Terminal and a Printer are available. Since the basic system is designed to be adaptable, it is easy to see that a configuration for a specific requirement is easily assembled.

Some final thoughts. I was impressed at the auto-booting of OS-9. I am sure that this is not unique to the CME 9687 system, but after punching in files at the start of each session, it was a pleasant surprise. Transfer of large programs from the hard disk are fast. Stringdash (about 23K) is on the line in about 2 seconds. The 9687 also supports the CME 9614, 9617, and 9618 EPROM PROGRAMMING HEADS. If your needs include eprom burning, I have had in 9687 TABLETOP MICROCOMPUTER in operation for about 2 months without any problems. It is still a learning process for me. I find it difficult to go back to the old system after having had a taste of new Relic Rayce class of computers.

Sincerely,



Anthony J. Gosses
Box 78
Sullivan, N.M. 87445
Ph (505) 647-7797

1313 N.E. Fremont
Portland, OR 97212
Phone (503) 234-2831
April 30, 1983

Dear Don,

Concerning ISC's PR.CMB, I made a few modifications that may be helpful. It still uses PRX 2.0 and can't find justification to do the PRX route. I processed my thesis in Cambridge there were several times that only the first few letters in a word needed to be underlined (to mean italics) and these letters were lower case.

First, the "CLSPY" subroutine called by *SET UNDERLINED CHARACTER on page 16 of the manual needed to be moved so that lower case would be allowed. This new CLSPY was moved to 12500 to be out of the way and simply doesn't force upper case by subtracting 32.

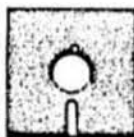
Next, by using a DUMP AND REPAIR utility, or by modifying the EDITOR, set J20 (parity). I used NLITE by MICROPI, since the source was handy, and str-L after the letter to be underlined works well for me.

Strangely believe it, there are people using the 68001 and we appreciate your journal.

Yours truly,

Darry
Gary Lemoine

'88' Micro Journal



Melkust Ltd.

MICROPROCESSOR SYSTEM, DESIGN AND PRODUCTION

35a Guildford Street, Luton LU1 2NQ, Bedfordshire, England.
Telephone: 0582 418028
Telex: 87628 MELKST G

Your Ref:

Our Ref: SP/SA/4065

Date: 20th, January, 1983.

Computer Publishing Centre,
68, Micro Journal,
5900, Cassandre Smith,
P.O. Box 849,
Milton,
1977 253,
U.S.A.

OS9 LEVEL 1 - FOR MOTOROLA EXORSET 30/33

Dear Sirs,

Melkust Ltd. of Luton (U.K.) are pleased to announce a series of easily installed upgrades for the Motorola Exorset 30/33, which enable the use of Microware's OS9. This implementation, unlike those before it was standard OS9 diskette format, making the purchase and use of software from any source possible.

Please contact Bid Price at the address below for details or phone/telex on (0)582 418028, telex 825828 (U.K.).

Melkust Ltd.,
35a, Guildford St.,
Luton, LU1 2NQ,
Beds.
U.K.

Yours faithfully,



S. Price
Director - Melkust Ltd.



P R E S S B L E A S E

Treat that itchy joystick finger to something special with the newest game release from Mark Data Products. GLAXIONS is a super hi-res space game for the Radio Shack Color Computer and TDP-100 that pits your playing skills against squadrons of swooping, diving enemy spacecraft. Your goal in this fast and furious game is to eliminate as many aliens as possible while avoiding your own destruction - not easy! Seven selectable skill levels coupled with automatic game acceleration provide a challenge for both novice and expert players.

This machine language program is available on 16K cassette for \$24.95 and 32K disc for \$29.95. GLAXIONS is available at your favorite dealer or may be ordered from Mark Data Products, 24001 Alicia Parkway, Suite 207, Mission Viejo, CA 92691.

24001 Alicia Pkwy. No. 226 • Mission Viejo, CA 92691 • (714) 768-1551

HI DON:

FIRST LET ME TELL YOU HOW MUCH I ENJOY YOUR MAGAZINE.

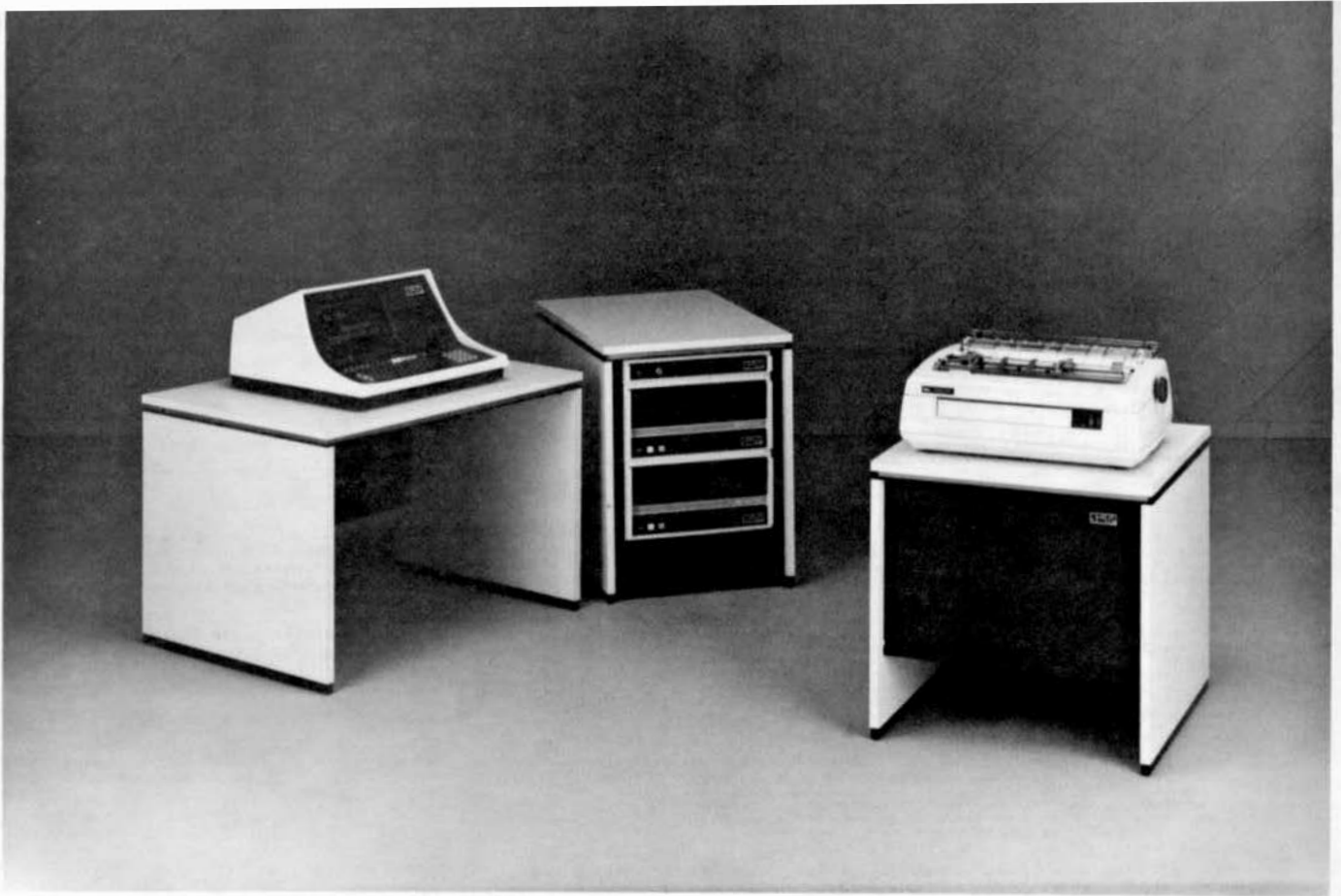
I HAVE BEEN A SUSCRIBER SINCE MID 79. I STILL USE THE BACK ISSUES FOR REFERENCE MANY TIMES. ALSO WAS VERY PLEASED TO SEE PETE STARK IN THE MAGAZINE TOO. HAVE FOLLOWED HIM FOR MANY YEARS.

I HAVE BEEN LOOKING FOR A COMPREHENSIVE RTTY PROGRAM FOR MY

6800 OR 6809 WITH DISK BUT TO NO AVAIL. MAYBE YOU CAN HELP

IF SO WOULD LIKE TO HEAR FROM YOU, OTHERWISE WILL KEEP LOOKING

AT THE MICRO-JOURNAL FOR IT. I HAVE A 68K SATE BOX, 2 5 1/4 DRIVES.



THE COMPLETE BUSINESS SYSTEM

+ Multiuser + Highly Expandable + Cost Effective

S+ THE CONCEPT

The S+ system is a modular computer system in which all portions of the hardware and software are designed to work together in the most efficient way possible. An S+ single user system with floppy disk storage is a competitive and cost effective entry level system. Unlike most other small computers being sold as "personal", or "small business" machines, the S+ system may be expanded to maximum capabilities using this same hardware and software. You cannot end up with a DEAD END system that cannot be expanded and whose software is not compatible with larger machines. A basic S+ system may be expanded to thirty-two users, a megabyte of main memory and hundreds of megabytes of hard disk storage by simply plugging in, or connecting the desired upgrade equipment.

TOTAL DESIGN—Hardware and Software

The S+ system is an integrated hardware and software design. The two complement and enhance each other in this system. The UniFLEX® operating

system used in the S+ systems is patterned after the Bell Laboratories UNIX® operating system, one of the most admired and widely used operating systems in the world. Instead of being an afterthought, the software is part of the design of the S+ system. You can be sure that with this approach that all parts of the computer operate with maximum efficiency and cost effectiveness.

THE CENTRAL PROCESSOR

The basic S+ system is configured with 256K bytes of memory and can be expanded to more than 1 million bytes. An efficient and fast hardware memory management system is used to allocate the available memory among the users on a dynamic basis. As little as 8K bytes, or the entire memory—if needed—can be used by any individual user. This makes it possible to run very large programs on the system, but it also uses no more memory than necessary for a particular job. The increase in cost effectiveness of this system over crude and outdated bank switching arrangements is dramatic.

The central processor runs in both user and supervisor states. It can detect and reject a defective user program. It is impossible for a user program to go bad and stop the entire system, as can happen quite easily in less sophisticated systems.

Task switching is accomplished by use of a multiple map RAM memory, with sixty-four individual task maps. Each task can access from 4 to 64 K-bytes of memory. Multiple tasks may be used in programs that require more than 64K bytes of memory for execution. When a task is completed the memory is automatically released for other use.

SOFTWARE

The S+ operating system, UniFLEX® is a multiuser, multitasking operating system based on the UNIX® operating system that has been used for many years on Digital Equipment Corp. PDP-11 series minicomputers. It is considered one of the most sophisticated and "user friendly" operating systems available. Variations of UNIX® are rapidly becoming standard on mini and larger microcomputers.

A large variety of languages are available for use with the system. These include FORTRAN, COBOL, BASIC, and Pascal. Word processing packages are also available to give you full text processing capability on the system.

Applications programs are available in large quantities in many fields. This includes general business, medical, dental, veterinary, library and real estate management; plus others. Since the system is multiuser it can also be connected to cash registers to produce a point-of-sale terminal system combined with the computer. The possibilities for application of this system are endless.

THE I/O SYSTEM

The S+ system is totally interrupt driven. All terminal and printer I/O devices connect to an I/O bus separate from the main bus. Up to thirty-two separate devices may be connected to the I/O bus at any one time. If I/O activity is great enough to cause an unacceptable slowdown in system operation, a separate I/O processor can be installed in the system. This plug-in option removes all I/O handling

overhead from the main processor and allows operation of up to thirty-two external devices at 9,600 baud. Without an integrated total design, as in the S+ system, it would become impractical to use a UNIX® type operating system in a situation with heavy terminal I/O activity.

DISK STORAGE

A wide range of disk storage capacity is available for the S+ system, from 2.5 M-byte floppy disks to an 80 M-byte Winchester and many sizes between. All disk controllers use direct memory access (DMA) type operations to maximize data transfer and to minimize overhead on the main processor. The Winchester disks also use intelligent controllers along with DMA transfers to preserve the performance that these type devices are capable of giving. Without this distributed intelligence the system performance would be greatly degraded. The UniFLEX® operating system is designed to work at maximum efficiency with this type disk system. The data transfer rates achieved by this combination rival those of large minicomputers.

COMMUNICATIONS

A high speed local network communications system is available to interconnect S+ systems. The VIA-BUS® network will allow communication between systems at data rates of over 400K baud. Such a system makes it possible to share data between local systems in an efficient and low-cost manner.

AVAILABLE SOON

Tape backup—20M-Byte in less than 15 minutes on a standard ¼ inch cartridge.

Mini-Wini—5 and 10 M-Byte Winchesters—5¼ inch package. Winchester performance, for smaller systems in a small package. UniFLEX® compatible design.

Large Capacity—190 and 340 M-Byte Winchesters, plus SMD cartridge drives.

UniFLEX is a registered trademark of Technical Systems Consultants, Inc.

UNIX is a registered trademark of Bell Labs.

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THAMES DON.

David F. Balko
DAVID F. BALKO
3005 CONCORD
TRENTON, NJ 48183
(313) 676-6856

Dear Don,

I am writing to ask You or one of Your reader
for VTL-09, a small BASIC-like language for
the MC 6809 μ P. VTL-09 is mentioned in
"THE MC 6809 COOKBOOK" (TAB-Book 1209; p. 118)
by Carl O. Warren.

Please could You tell me an address or reference,
where I can get a version of this nice tool.

With best regards

Klaus-Dieter Leyhr

Klaus-Dieter Leyhr
Roonstr. 17
0-7500 Karlsruhe 1
WEST GERMANY

I HAVE BOUGHT TWO VERSIONS OF FLEX FOR THE COCO.
THE 'DATA-COMP' VERSION - F-MATE SEEMS TO WORK
MUCH, MUCH BETTER. IT READS MORE DISK FORMATS
(SWTPC, OIMIX, ETC) THAN THE OTHER, WITHOUT A
LOT OF HASSLE. IT ALSO COMES WITH THE TSC ASSEMBLER
AND EDITOR INCLUDED. THE SCREENS ARE MUCH EASIER
TO USE AND READ. ALSO I HAVE HAD TO CALL BOTH, AND
THE RESPONSE HAS BEEN GOOD AT DATA-COMP FOR F-MATE.
THE OTHER I HAVE TALKED TO LADIES WHO DON'T KNOW
MUCH ABOUT FLEX OR COMPUTERS, I STILL AM HAVING
TROUBLE WITH THE FML VERSION. HAVE NOT TRIED THE
OTHERS, CAN NOT COMMENT.

IF YOU ARE LOOKING FOR A "GOOD" VERSION OF FLEX FOR
THE COCO THEN I RECOMMEND THE DATA-COMP F-MATE. IT
DOES NOT 'COST' ANY MORE AFTER YOU BUY THE 'EXTRAS'
FOR THE OTHER...THE F-MATE IS COMPLETE!

BOB LASSINGER - LaGRENGE PARRISH, LA

P.S. AM LOOKING FOR A GOOD SOURCE FOR A CP/M TO FLEX
PROGRAM THAT WILL RUN ON DATA-COMP F-MATE FLEX, ON
MY COCO. I HAVE TWO DISK DRIVES (DD,DS). I HAVE A
LOT OF CP/M SOFTWARE THAT I HOPE TO RUN ON THE 'MICRO-
SOFT' BASIC IN MY COCO. I CAN GET IT ON A COCO DISK
WITH THE PROGRAMS I GOT WITH MY DATA-COMP FLEX (RSOIR,
RSREAD, RSWRITE), THEY WORK FINE. I JUST NEED SOME
WAY TO GET THEM OFF MY 9" CP/M DISK. THANKS - WILL
CHECK BACK IN EACH WEEKEND. ANOTHER P.S. - I SURE
APPRECIATE FLEX OVER CP/M. LIKE GOING FROM A DARK
ROOM, TO OUTDOORS INTO THE FULL SUN SHINE!!!!!!!!!!!!

PRODUCT RELEASE

For release after April 15

For additional information contact: Matt Astengo

NEW MICROPROCESSOR WORK STATION INCLUDES EMULATOR AND 32 CHANNEL LOGIC ANALYZER

Bellevue, Washington, USA: The MODEL 4009 by Advanced Digital Technology combines an 8 bit Emulator with an analyzer-like 46 bit by 2048 line Trace, and an independent 32 channel by 1024 state Logic Analyzer, creating a single, easy to operate Microprocessor work station. These elements operate interactively or separately, under control of an independent microprocessor, with input and display provided by an operator-supplied video terminal. This combining of instruments eases and speeds the time consuming task of "integrating" microprocessor-based products.

ADVANCED EMULATOR PERFORMANCE

Newly developed emulation techniques provide bus timing characteristics that more closely match those of the microprocessor than was previously achievable. These techniques include:

- 1) Predictive clock generation by a proprietary tracking oscillator.
- 2) Stabilization of emulator timing through cooling of the processor.
- 3) Speed-up of time-critical signals by reactive loading.

Control of the Emulator is menu-orientated, enabling the operator to examine and store to memory, examine and alter registers, transfer data to and from Development Systems, test memory and buses, mask and generate reset and interrupts, trap on illegal instructions, begin emulation at any program point, run-to-breakpoint, and monitor clock and target system voltage. All microprocessor features are supported and the command menus may be used without effecting Emulator operation.

EXTENSIVE EMULATOR TRACE CAPABILITIES

A 46 bit by 2048 line Emulator Trace may be operated without interference of Emulation or may be set to stop emulation upon the occurrence of a sequence of events. 48 operator-defined events may be predefined and stored. Each event represents a pattern of 21 Address, 8 Data, Read/Write, 2 Bus- States, Illegal Instruction, 2 cycle-identifier and 4 Logic Qualifier bits, each definable as Logic 0, 1 or X (don't care).

Up to four events detected by hardware comparison may be used to specify a Trace Sequence, Restart Sequence and Trace Only Events. Each event may be passcounted up to 65535 times prior to detection. The trace is programmable to capture desired bus activity with selective exclusion of bus-available cycles, DMA operations, invalid addressing (non-VMA) or irrelevant cycles.

An extensive choice of commands for the display of Trace, are available to perform comparisons of program activity with a previous trace record, to search for patterns or to disassemble program execution.

Single-step operation is also provided in trace display, or during register display.

LOGIC ANALYSIS FOR MULTI-PROCESSORS AND I/O

An optional 20MHz, 32 channel by 1024 state Logic Analyzer allows analysis of any desired points within the target system. Records are acquired either synchronously or asynchronously with respect to the Emulator and displayed independently or in a combined format with Emulator trace records. When displayed together, activity traced in both records is shown in order of real-time occurrence. The Logic Analyzer may be clocked from any of five internal sources, or from an external variable-threshold input. To provide cross-triggering, the passcounted events from the Emulator Trace are included in the Logic Analyzer event structure and events detected by the Logic Analyzer may become part of the event field of the Emulator Trace.

A MODULAR PACKAGE WITH FLEXIBLE I/O

The MODEL 4009's metal enclosure measures 17.5 by 15 by 5.75 inches and includes an Emulator card set and Pod assembly, Emulator Trace, Supervisory controller and one RS-232 port. Ordering options include Emulator card sets, Logic Analyzer card set and Pod Assemblies, 16K or 32K-Byte Memory Overlay, mappable in 512 Byte Segments, and optional I/O port cards which allow connection to a Host Computer, Prom Programmer and serial or parallel printers. 6809 and 6809E processors are currently supported with a single Emulator card set with other microprocessor support to follow. Delivery 30-60 days ARO, Price \$6500 and up depending on options.

ADVANCED DIGITAL TECHNOLOGY, INC., 13400 Northrup Way, Bldg. 27,
Bellevue, Washington 98005 (206) 643-2382

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'68' Micro Journal

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FORTH AND CERTAIN OTHER LANGUAGES USE REVERSE POLISH NOTATION DURING THEIR ARITHMETIC EVALUATION ROUTINES. INDEED, POSTFIX OR REVERSE POLISH NOTATION AS IT IS CALLED BY HEWLETT PACKARD, IS THE MOST EFFICIENT METHOD OF REPRESENTING ARITHMETIC EXPRESSIONS FOR A COMPUTER. ALMOST ALL COMPILERS CONVERT THE STANDARD ALGEBRAIC (INFIX) NOTATION TO POSTFIX BEFORE GENERATING MACHINE CODE FOR IT. THIS ROUTINE, WILL TAKE AN INFIX STRING CONTAINING AN EXPRESSION, AND CONVERT IT TO A POSTFIXED STRING. THE RESULTING STRING WILL HAVE \$OO SEPARATING EACH OF IT'S ELEMENTS, INCLUDING VARIABLE NAMES AND OPERATIONS. THE LOCATIONS WITHIN THE PROGRAM THAT PRODUCE THE SEPARATORS ARE CLEARLY MARKED, SO THAT YOU CAN USE ANY KIND OF SEPARATOR THAT YOU WANT. FOR EXAMPLE, YOU MIGHT WANT TO USE ,S TO MAKE THE EXPRESSION COMPATIBLE WITH THE INPUT TO A MACRO ASSEMBLER, LIKE SSB'S MACRO ASSEMBLER. THIS ROUTINE USES A STACK TO PARSE THE EXPRESSION. IT DOES NOT ALLOW FOR LOGICAL OPERATIONS, THOUGH IT WOULD BE EASY TO MODIFY SO THAT IT DOES. LISTING 2 HAS THIS DONE. I SELECTED THE &=AND, !=OR AND ~=XOR. YOU CAN CHANGE THESE TO WHATEVER SYMBOL YOU WANT, WITHOUT MESSING UP THE OPERATION OF THE PROGRAM. I GAVE THE LOGICAL OPERATORS THE SAME PRIORITY AS ADDITION AND SUBTRACTION.

IT SHOULD ALSO BE POSSIBLE TO INCLUDE COMPARISON OPERATIONS ALSO. THE PROGRAM IS TREATED AS A SUBROUTINE, AND IS CALLED WITH X POINTING TO THE INFIX STRING, AND B HOLDING THE LENGTH OF THE STRING. IT RETURNS THE X REGISTER POINTING TO THE POSTFIX STRING. IT IS WRITTEN FOR THE 6800. IF I WERE WRITING IT FOR THE 6809, I WOULD USE THE USER STACK FOR THE PARSING STACK, THE X REGISTER FOR THE INFIX POINTER, AND THE Y REGISTER FOR THE POSTFIX POINTER. I WOULD APPRECIATE HEARING FROM ANYONE WHO USES THIS ROUTINE, AND IF ANYONE MAKES ANY IMPROVEMENTS TO IT. PLEASE SEND ME A COPY.

```

1.00-
2.00- THIS ROUTINE TAKES AN INFIX STRING AND CONVERTS IT TO A
3.00- POSTFIX STRING. IT ALLOWS N CHAR VARIABLE NAMES, AND UP TO
4.00- TRIPLE PRECISION CONSTANTS. IT SUPPORTS *,/,-, AND !
5.00- X MUST POINT TO THE ASCII STRING TO BE CONVERTED. THE X
6.00- REG. RETURNS POINTING TO THE ASCII STRING IN THE POST FIX
7.00- REGISTER B HOLDS NUMBER OF CHARACTERS IN INFIX STRING
8.00-
9.00-      ORG      $0100
10.00- DRIVER  LDX   #IFSTR      ;INFIX STRING.
11.00-          LDA   B   #19
12.00-          JSR   PFIX
13.00-          JMP   $E000 ;GOTO MONITOR
14.00- IFSTR    FCC   'A=(B+C*21)/(G/D-8)'
15.00-
16.00- * INFIX TO POSTFIX CODE PRODUCER
17.00-
18.00- PFIX     EQU      * SET SPACING UP FOR THE LINE.
19.00- START    STX      IFXPTR  POINT TO INFIX STRING
20.00-          CLR      STKLEV   SET STACKLEVEL=0
21.00-          STA      B        UP TO 255 CHARACTERS
22.00-          LDX      @PFIX1   POINT TO THE POST FIX LOCATION
23.00-          STX      PFIXPT   SET POINTER TO POST FIX STRING
24.00- PTOP     TST      IFONT    SEE IF INFIX IS EMPTY
25.00-          BGT      NOTEMP
26.00-          LDX      PFIXPT   CLEAR STACK TO PFIX

```

```

27.00=CLEAR PUL A
28.00=STA A 0,X
29.00=DEC STKLEV
30.00=BEQ END CLEAR ONWARD.
31.00=INX
32.00=BRA CLEAR
33.00=END LDX PFIXPT GET POINTER TO THE INFIX STRING
34.00=RTS
35.00=
36.00=
37.00=NOTEMP LDX IFXPTR POINT TO THE INFIX STRING
38.00=MLOP LOA A 0,X GET A CHARACTER
39.00=INX
40.00=STX IFXPTR
41.00=CMF A #' '
42.00=BEQ MLOP SKIP SPACES.
43.00=LDX PFIXPT POINT TO WHERE TO STORE THE CHARACTER
44.00=CMF A #'A' TEST FOR VARIABLE
45.00=BLT SKIPCH
46.00=CMF A #'Z' TEST OTHER END
47.00=BGJ SKIPCH INVALIO VARIABLE
48.00=LLOP STA A 0,X MOVE VARIABLE NAME
49.00=INX
50.00=CLR 0,X TOO IS SEPARATOR
51.00=CLR 1,X
52.00=STX PFIXPTR POINT TO THE INFIX STRING
53.00=DEC IFXONT
54.00=LOA A 0,X KEEP ANALYZING STRING
55.00=INX
56.00=STX IFXPTR KEEP POINTERS UP TO DATE
57.00=LDX PFIXPT
58.00=CMF A #'A' SEE IF MULTI CHARACTER VARIABLE NAME
59.00=BLT SKIPC2
60.00=CMF A #'Z'
61.00=BGJ SKIPC2
62.00=BRA LLOP AS LONG AS ALPHA CHARACTERS, MOVE VAR.
63.00=
64.00=
65.00=SKIPC2 INX LEAVE A SEPARATOR
66.00=SKIPCH STX PFIXPT SAVE POST FIX POINTER
67.00=LDX IFXPTR POINT TO CORRECT TOKEN
68.00=DEC IFXONT REMOVED 1 FROM INFIXED STRING
69.00=CMF A #'(' SEE IF PARENTHESIS
70.00=JNE MORP
71.00=PUSH PSH A IF IT IS THEN STACK IT
72.00=
73.00=INC STKLEV
74.00=PTOP1 BRA PTOP
75.00=PULL PUL A UNSTACK IT
76.00=DEC STKLEV
77.00=BRA PTOP
78.00=
79.00=MORP CMF A #'(' SEE IF OTHER
80.00=JNE SEMPTY CHECK FOR STACK EMPTY
81.00=BRA OPL0P
82.00=
83.00=
84.00=OPL0P1 PUL A GET A
85.00=DEC STKLEV KEEP UP WITH STACK LEVEL
86.00=LDX PFIXPT
87.00=STA A 0,X
88.00=INX
89.00=STX PFIXPT SET IT BACK UP
90.00=OPL0P PUL A
91.00=PSH A GET THE OPERATOR
92.00=CMF A #'('
93.00=JNE OPL0P1 WIPE NEXT ONE
94.00=PUL A
95.00=DEC STKLEV
96.00=BRA PTOP1
97.00=
98.00=
99.00=SEMPY TST STKLEV CHECK STACK LEVEL
100.00=BEQ PUSH IS EMPTY
101.00=PUL A GET TOS
102.00=PSH A FIND TOS
103.00=CMF A #'('
104.00=BEQ PUSH2 A HOLDS THE VALUE..X->OPERATOR
105.00=CLR PRIOR1
106.00=CLR PRIOR2
107.00=BSR PRIOR1 CHECK PRIORITY
108.00=LDX A PRIOR2
109.00=STA A PRIOR1 TAKE OPERATOR PRIORITY AND SAVE
110.00=CLR PRIOR2
111.00=DEX
112.00=LOA A 0,X GET THE OPERATOR
113.00=INX
114.00=BSR PRIOR1
115.00=LDX B PRIOR1
116.00=LDX A PRIOR2 GET THE PRIORITIES
117.00=CBA COMPARE
118.00=BGJ PUSH2 IF 2>1 THEN PUSH 2
119.00=PUL A
120.00=DEC STKLEV
121.00=LDX PFIXPT
122.00=STA A 0,X
123.00=INX
124.00=STX PFIXPT
125.00=BRA SEMPTY STACK EMPTY
126.00=
127.00=
128.00=PUSH2 OEX FIND PREVIOUS OPERATOR
129.00=LOA 0,S
130.00=INX
131.00=BRA PUSH
132.00=PRIOR1 CMF A #'>
133.00=BEQ EXP

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134.00=CMF A #'>
135.00=BEQ MLOP
136.00=CMF A #'/'
137.00=BEQ MLOP
138.00=CMF A #'+'
139.00=BEQ ADDOP SECOND LOWEST PRIORITY
140.00=CMF A #'-'
141.00=BEQ ADDOP
142.00=CMF A #'*'
143.00=BEQ ASSGN
144.00=BSR RETURN MAY WANT AN ERROR MESSAGE
145.00=*****
146.00= CALCULATE PRIORITY ROUTINE. NUMBER 4=HIGHEST 1=LOWEST *
147.00=*****
148.00=*****
149.00=EXP INC PRIOR2 SET PRIORITY UP
150.00=MLOP INC PRIOR2
151.00=ADDOP INC PRIOR2
152.00=ASSGN INC PRIOR2
153.00=RETURN RTS RETURN WITH PRIOR2=PRIORITY NUMBER
154.00=*****
155.00= VARIABLE ALLOCATION BLOCK *
156.00=*****
157.00=STKLEV RMB 1
158.00=IFXONT RMB 2
159.00=PFIX1 RMB 256
160.00=PFIXPT RMB 2
161.00=PRIOR1 RMB 1
162.00=IFXPTR RMB 2
163.00=PRIOR2 RMB 1
164.00=END DRIVER

```

COMPARE - 6809 - Z80

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Introduction

I would like to help set into perspective some of the facts, myths, and innuendo that surround the on-going comparison between the 6809 software world and the 6809 hardware world. This discussion on numerous occasions has become a good case versus bad case type of dialogue characterized by rather emotional "taking of sides". I, of course, will stand as accuser of harbouring subjective opinion (and correctly so) but perhaps with a slight difference I have implemented from the ground up and owned both a Flex based 6809 system and a CP/M based Z80 system of comparable configuration, cost, and capability. In the process of implementing and subsequently using both systems, I have accumulated literally hundreds of hours of exposure to the internals of each. Professionally, I am employed as a technical engineer with one of the "big five" computer companies and currently have responsibility for support of four major operating systems - two minicomputer based and two mainframe based, so hopefully my comments will be of use and of interest to the readers of '68' Micro Journal.

The comparisons range on at several different levels: the actual processing elements 6809 vs. 8080/8085/Z80, the respective bus architectures 8-30 vs. 8/100, the operating systems Flex or 68e vs. CP/M, and the applications, languages, and utilities which are available for each. Each camp carries a knowledgeable and relatively large body of users which are supported by their respective financial publications - '68' Micro Journal for the 6809 folks and Byte magazine for the CP/M people. I subscribe and read every issue of both and frankly am more than happy with each.

Comparing the Microprocessors

Let's start with the chips but with one proviso: this is not going to be a hardware oriented discussion as I will defer to others much more qualified than I in that regard. In my opinion, from a register architecture and instruction set point of view, the 6809 is hands down far and away the most elegant processor in the 8 bit world. Although the 8080 and particularly the Z80 at first glance offer a wider range of registers, the 6809 really compensates with the simple elegance of its instruction set and addressing modes. It is true that the Z80 offers a superset of the functionality of the 8080 or 8085 devices, but an often overlooked fact must be remembered. Beldos in this entire functionality used as code by who write CP/M software do so for the largest possible target audience and therefore limit their instruction usage to 8080 instructions only. Using Z80 specific functions would preclude usage by CP/M users with 8080 or 8085 processors. The moral of the story is that the Z80's true potential is rarely exploited. Note that CP/M is officially shipped by Digital Research (its author) for usage in an 8080 environment. Last but not least, the 6809 has a vast advantage in its ability to address memory beyond the 64K limit of the other 8 bit processors.

Both chips are manufactured by large and professional semiconductor manufacturers, and both processors are well supported with a family of companion chips such as input/output controllers, DMA controllers etc. The quality and quantity of documentation in each case is generally superb.

Catching a Bus

Neither chip is by definition tied to a specific physical bus architecture, but in each case a de facto bus organization has emerged. Note that not all 6809 computers use the 8-30 bus nor do all 8080/Z80 computers use the 8/100 bus. Many major manufacturers have elected to not incorporate a bus design into their computer as a cost and space saving decision. For example neither the Radio Shack Z80 computers or the Xerox computers are bus oriented. In the physical sense of the word, such computers almost invariably suffer from a lack of flexibility and expandability as a result. I can not debate the merits of 8-30 vs. 8/100 from an architectural point of view, but it must be noted that the 8/100 bus has been adopted by the IEEE as an industry standard. And lastly, it must be realized that (horrors!) there is no reason why a 6809 chip can not be placed on the 8/100 bus. In fact, I know of at least one manufacturer (Ackerman Digital) which has done just that.

Anyone for Assembler?

If you are anticipating doing much assembler work, I assure you that the 8080 world is preferable from a development point of view. The problems with the 8080/280 assemblers are something like this. First at Intel who manufactures the 8080 and 8085 defined the assembler and assembler syntax for the 8080 in an arbitrary fashion as of course was their right to do. The only problem is that they did a horrible job of it. Now Zilog (who is the manufacturer of the 280) introduced their superior 8080 and called it the Z80. Clearly for the instructions that did not exist on the 8080, Zilog needed to introduce new mnemonics. However, they rightly decided that the Intel mnemonics were less than ideal and therefore came out with an entire new and much better list of mnemonics which were totally incompatible with the original Intel list. Of course the software houses as well as both Intel and Zilog wrote assemblers to support the various mnemonics. At least one such software house made further changes and introduced more confusion. Gary Killest who is president of Digital Research and an ex-Intel man has done a great deal with the Intel mnemonics and indeed all the assemblers from Digital Research steadfastly refuse to assemble the Zilog mnemonics unless you set a macro definition library that defines the Zilog mnemonics as macros and provides them into appropriate Z80 instructions. Of course you need the macro assembler to do this, and of course the assembler provided when you buy CP/M is not the necessary macro assembler and on and on it goes. Needless to say, I have a or 3 assemblers that I use when working with CP/M. Some support Intel, some Zilog, one supports both, some support macros, and two produce relocatable loadable code. Believe me, there are days when I long for the atrociously worded TBC assembler.

How Fast Will It Go?

Having said so much good stuff about the 8080 elegance and architecture, you might then assume it outperforms the 280 in processor speed benchmarks. Well, not always, for purposes of comparison, I will reference the infamous benchmarks conducted over a period of two years by Jim and Gary Gilbreath the results of which were originally published in the November 1981 (V.10#4) issue of Byte magazine, and recently updated in the January 1983 (V.12#1) issue of the same magazine. Where possible I have personally confirmed the results on my own hardware.

Bilbreath's benchmarks used a variation of the classic Sieve of Eratosthenes algorithm for computing all the prime numbers between 3 and 10,381. Other than the fact that the algorithm was chosen to avoid division, the algorithm itself is arbitrary as this was not a contest to determine the quickest way to compute prime numbers. The algorithm was coded as consistently as possible in each of several languages and compiled and/or interpreted as necessary on many different processors and computer systems. In order to get a more meaningful result, the algorithm was repeated 10 times, however for the extremely slow machines or languages, the algorithm was run only once and the resulting times multiplied by 10 to allow proper comparison. I will contain myself to summarizing 8080 and 280 results although everything from Amivide to the Cray-1 super computer were exercised. Note that other than assembly languages, the results are a reflection upon the relative efficiency of the compiler or interpreter as well as providing a comparison of the processors.

assembler	8080 2MHz	280 4MHz	6MHz
IMS Pascal (compiler)	5.10 sec	6.80 / 4.50 sec	
Introl C (compiler)	8.78 sec		
TBC Pascal (Uniflex compiler)	11.00 sec		
TBC Pascal (old compiler)	34.00 sec		
IMS Pascal (P-code)	54.00 sec		
BASIC 09-105-B interpreter	105.00 sec		
Dmscode Pascal (B-code I think)	238.00 sec		
Lucidata Pascal (P-code)	305.00 sec		
TBC xBASIC (iflex interpreter)	840.00 sec		
FORTRAN (Microsoft compiler)	13.90 / 9.20 sec		
PL/1 (Digital Research compiler)	14.00 / 9.33 sec		
C800 (Digital Research Basic Compiler)	15.70 / 10.46 sec		
Pascal x86 (Digital Research compiler)	19.00 / 12.86 sec		
US80 Pascal (P-code)	22.00 / 15.33 sec		
J80 Pascal (P-code)	383.00 / 255.32 sec		
CBASIC (Digital Research interpreter)	484.00 / 322.66 sec		
CBASIC (as above but all reals)	1420.00 / 953.32 sec		
C800L (Microsoft)	3115.00 / 2410.00 sec		

The above chart lists popular 8080 and popular 280 languages without their respective results. Now, I suspect that no one takes all of the above too seriously as there is a certain amount of arbitrariness and/or comparison here. And of course, one small algorithm hardly comprises a thorough evaluation. However, some interesting observations can be made. First of all, C800L does not do one on 8 bit computers unless you have a lot of time on your hands.

At the assembly level, the 8080 is a very competitive with the 4MHz 280. In fact substantially quicker on a percentage basis. But when we compare the compiler and interpreter, it becomes apparent that the 280 users are most often enjoying a huge speed advantage. Nearly all of the well known and widely used 280 compilers produced results in the 14 to 20 second range when run on the 4 MHz 280. Yet on the 8080, only the Introl C compiler and the IMS Pascal compiler were in this league and in fact again somewhat quicker. The TBC Pascal compiler while not comparable to the Pascal NT compiler was at least

reasonable. The real price/performance winner in the Pascal world is the J80 P-code implementation which came as a complete release for \$29.95. In defense of Lucidata, I believe (at least I hope) that the run was made on a 1 MHz 8080 as this was not clear.

What conclusions if any can be drawn? It appears that the 8080 is quicker than the 4MHz 280, but not quite as quick as the 6MHz model. Now about a 4MHz 8080 vs. Motorola's 68000. And I must word this carefully. It appears that the 280 CP/M user has a broader range of very well implemented languages from which to choose. Now I know that some of the 8080 compilers produce rather inefficient code and some of it is re-entrant and all of that may impact performance, but most of the CP/M languages allow for personal a language step that will relocate the code for you if you so desire. The 8080 user has most of the regular languages from which to choose, but amongst these, there are fewer really excellent implementations. The implementation is very important - just compare the IMS P-code Pascal results with other P-code implementations on the same processor. And last the compiler builders declare war on me. I should also say that the speed of the resulting object code is only one measure of the quality of the compiler. Speed of compiler, rate of assembly, execution code etc. are other important considerations when comparing compilers.

The Operating System

Unfortunately, my comments must be directed toward Flex on the 8080 side and CP/M release 2.2 on the 280 side as my experience is limited to these two. But first a few words about the other most often used operating systems on these machines.

While I have not used either Q89 or Uniflex, my understanding from discussions I've had with those who have indicates that they are very good. Either CP/M or Flex in many ways is really as a result of successfully implementing much of the Unix environment. Also in the "user beyond" category is an 8080/280 operating system known as OASIS. All of these more advanced operating systems allow multiple users and tasks and also implement a more sophisticated file system. Actually, CP/M itself has a multi-user brother known as MP/M. Not nearly as widely used as CP/M and requiring multiple sets of memory to accommodate the multiple users, MP/M offers little in the way of features beyond CP/M. There have been a few CP/M clones over the years. The first I believe being 1980 from Cromemco, and more recently a version known as TurboDOS which features such improved disk I/O at least from a speed point of view. And of course, Liveball Associates distributes CP/M under its own name SE-80. That leaves only the much maligned 18S-DOS (more often referred to as Trash-DOS) which Pades Shack supplies with its 280 computers.

Back to Flex and CP/M. As a blanket statement, it has been my experience that Flex is easier to implement, use, and train non-computer people to use. Indeed, CP/M at times is seen to be so convoluted that at least two companies serial major products designed to insulate the casual user from the complexities of CP/M. These products (SuperVox and Organizer) achieve this by presenting a series of menu displays which are customizable. They allow the user to select by number the function they would like to use which is then translated into CP/M commands.

Both offer provide a single directory disk, but only Flex has the date of last update maintained in the directory. Both allow attaching attributes such as sequential directories linking to individual files. But only CP/M allows a whole diskette to be write protected under software control as well as (optional) hardware control. A major failing of CP/M is a provision to search for filenames similar to Flex's concept of system and work drives. Flex itself is maintained as a file under the file system which is read at boot time, whereas CP/M is stored physically upon the first 2 sectors of the diskette. There are too problems with this: first the code (compared to CP/M) can not be treated as a file by the system utilities thereby adding necessary special utilities to maintain this code and its special area of the disk, and second, this disk space is forever wasted even if CP/M itself is not on the diskette.

CP/M allows arguments to be passed into its "subsystem" files (similar to "batch" files) which was a major shortfall in Flex. But on the other hand does not offer a "startup" facility. The main involved in making out CP/M to do an automatic startup would leave a Flex user seething. Also missing is the equivalent of Flex's "fixit" facility. No quick file builder like "build" either. Instead you get the infamous CP/M editor called Ed. The huge sales of Wordstar and other word editing utilities should give you an accurate impression of just how many people are willing to use Ed. (I'll reword my favorite of the old Motorola editor was back in the early 8080s days.)

I always liked the Flex spooling mechanism (after I got it working), but CP/M doesn't have one of those as standard running gear, there is one for an extra \$50 or so, but having once seen it implemented, I've never bothered buying it as it is convoluted and inferior to Flex's spooler. While we are on the topic of output, try to imagine using Flex without either the "q" or "w" commands to redirect output from you. Impossible for me. Not at all hundreds of thousands of CP/M users sit alone without it to be fair, you can cause console I/O to be copied onto the printer, but that is no substitute for I/O re-direction. This is possibly CP/M's greatest shortcoming.

On the plus side, is CP/M's famous "PIP" utility which replaces list, copy, append commands and still has features to burn. Briefly it copies (or sends) a file to a specified file or device. The source and destination may be any physical character oriented peripheral or a disk file. PIP allows you for example to type from the console keyboard to the printer, a modem, a diskette or to copy a diskette to the console, or printer, or modem or to another disk file etc. PIP and several other utilities allow specification of incomplete file names for the purpose of name matching, but perhaps PIP's greatest feature is the filters it can use on the data during the data transfers. Such as removing forwards or the parity bit in ASCII data, character case translation, expanding tabs, providing pagination, adding line numbers, etc. Flex has nothing even close to PIP.

CP/M supplies a utility called "DDI" which is a debugger and somewhat similar to the monitors we all expect to find living in EPROMS within our computers. DDI is useful insofar as it deals with the file system as opposed to the usual sector level I/O found in most EPROM monitors.

Due to the way in which the CP/M memory is arranged, CP/M must be at the top (high addresses) of configured real memory. But unfortunately, it can not relocate itself at boot time and therefore adjust to a system with less memory. Therefore one also finds with CP/M as utilities involved in tracking CP/M around in memory for other memory sizes. Having built a memory image of four new CP/M, another utility saves it out onto those two (internal) tracks mentioned above for posterity. While on the subject of memory wars, I must say that although I prefer the Motorola style of memory mapped I/O (MCA or PIA devices etc.) from a programmer's point of view, the Intel style doesn't cause the memory map to be hacked up simply to allow for the presence of addresses which are to be decoded by I/O devices. With the Intel approach or set a clean continuous 64K which CP/M is quite happy to use. A number of CP/M systems also use a "shootable" EPROM which after booting, turns itself off thereby allowing the full 64K of memory to be addressed. No reason why this could not be done on the 8080 system either.

File management is very similar from a user's perspective on both systems. Both provide sequential and random file organizations with sequential and relative record access methods. One very major difference is the way Flex and CP/M implement the file system. Flex defines a file as a linked list of sectors while CP/M allocates file space with a bit map technique. Which is better? Well I will say that in using Flex files and in writing sequential data, Flex is quicker. But CP/M may have the edge with random access. In any case, the speed differential is not that great based upon subjective observations not empirical measurement. However one thing I can say is that Flex diskettes are much more susceptible to corruption due to broken chains in the sector linkage. I have NEVER had a CP/M disk damaged. Both systems use the same disk drives (Shugart 801 series) so that is not a factor, however I admit the controllers are different.

At the system service level Flex's DOS routines and CP/M's 8080 routines while implemented quite different, largely provide the user with the same type of functions and are similar when it comes to ease of use. TBC has however extended DOS to include all sorts of wonderful little goodies such as routines to parse the input buffer. CP/M is much weaker in these areas.

From the Ground Up

I wonder how many of you have used ISC's general purpose version of Flex to implement your own flow system from the ground up? I have and it has been fun and easy. Nothing fancy about ISC's documentation, but it sure sets the job done. Flex's interface to the outside world is contained in two modules: the console I/O driver module, and the disk driver module which you must supply. This interface is the picture of simplicity and is very well defined.

If you have put up Flex in that style and are looking for a new challenge, use I recommend doing the same thing with CP/M. This is a job for those with stout arms and a penchant for documentation. I am sure you will find it as your native habitat. I apologize for comparing it with Digital Research's documentation. Unfortunately, you need to do all the same things: first your code, then write a loader, cross your fingers, and if all went well... It just seems an awful lot harder with CP/M.

Utilities, Languages, and Stuff

Both operating systems have available several other utilities and languages. ISC at a reasonably cost makes available a large set of programs: utilities and languages. On the CP/M side, because of its far greater success at ground level everybody and his brother has another language, editor, model/communications package, spreadsheet etc. for sale. However one of the greatest sources of fine utility software is the CP/M User's Group. Now I know that Ron Anderson and his cohorts (which I very much enjoy) are the core of the Flex user community, and that through the pages of *Micro Journal* a lot of utility software is published (and even available on disk from *Micro Journal*) but really, the CP/M group is massive. To date I believe they have distributed something in the order of 80-90 cassettes of public domain software. As you may imagine 75% of this is of less than commercial quality and often poorly documented etc., but the occasional gem is wonderful. For example there are thirteen or so languages, numerous directory display programs, a complete spelling checker, dozens of editors, business programs etc. If there is a Programmer's Hall of Fame, then a star by the name of Ward Christensen who seems to have written a great deal of the truly good stuff and unselfishly gave it to the User Group should be inducted without delay. Being able to access this wealth of goodies is quite important to me.

As I noted earlier, CP/M provides you with the basic Digital Research 8080 assembler, but its usefulness is somewhat limited. As indicated in the discussion on the spread sheet results, I believe that on average the CP/M user has a much wider selection of quality languages and utility software from which to choose thus allowing you to select the one that has the features you wish at the price you can afford. I've often said that the only reason I endure CP/M at all is just so that I can gain access to all the superb CP/M software. There have been a few comments passed in recent months regarding Wordstar in Ron's column. Wordstar whether you like it or hate it set the industry standard for full screen word-processing editors by which all others now appear to be measured. In Ron's case, he compares it to StarWriter. Ron's comments while correct are also misleading to a degree. It for example is possible to reformat the entire document with one command, but I admit there is a next of a lot of control sequences to learn. In any case, nobody says Wordstar is the "best" something which is "first" usually never remains "best" as others come along and improve upon the original concepts. There are now several WP packages for CP/M which claim to have more features and ease of use than Wordstar. By only command is if you don't like Wordstar or StarWriter or whatever, then find one you do like and use it. Which one isn't important; the productivity gains they provide are, this is being prepared with Wordstar.

I love the languages I can buy for CP/M. If you like Basic and are not embarrassed to use it after all the hate articles it has inspired over the last two years, CBASIC and its also brother CROB which is a subset of CBASIC's syntax plus fully compiled is the greatest thing since sliced bread. Microsoft who not to be famous by representing Basic, can not touch this stuff! Sorry to say, neither can ISC's XBASIC. At the far edge of the other side of the ideological spectrum there are a couple of extremely good Pascal compilers, and even some good P-code ones. Personally, I am in love with Digital Research's PL/I compiler which combined with the Linbase editor lets me do anything I can do on an airframe at work, and often outtrips my brain. For those of other inclinations, there are dozens of Fortran, a few ADA's, APL, COBOL for those foolish enough to try and use it, Fortran, LISPs, close to a dozen C compilers (my favorite for system software), literally scores of Basic's, etc.

It seems that between Flex, Unixflex, and OS9, the 6809 user has access to most of the popular software items of the day: ie. screen editors, spelling checkers, languages, sorts, spread sheet calculators and so on, but at the risk of being repetitive, I must admit CP/M users have a far greater choice and perhaps on occasion, better implementations.

The Future

Where do each of our two environments go in the future? At the moment, Digital Research is releasing CP/M release 3.0 which by all accounts fits and enhances CP/M to the point that it will compare at least favorably with Flex. Specifically, many of the complaints I have registered above have been addressed. However, I think it is safe to guess that release 3.0 may be the last release of CP/M for the 8080 as the 16 bit machines are attracting most of the attention of the software developers - Digital Research included. CP/M has now been implemented for both the Intel 8086 and the Motorola 68000 families. The subtle notion that somehow this is upward compatible from CP/M on the 8080 is totally false. Only the user interface and disk formats stay the same although a program (non-assembler of course) could be transportable at the source level. CP/M will not likely ever dominate the 8086 world in the way it completely ruled the 8 bit 8080 machines. Alreadr, Microsoft's MS-DOS (PC-DOS) if you prefer is leading the way in this arena. CP/M is already dead, and I can't really see anyone wanting to run CP/M on a 68000, who bother when far superior operating systems including full Unix are available. Presumably Unixflex will as per ISC's promise finally run on the 68000 as well.

In a similar vein, most of us would agree that Flex as we know it has reached its maturity and should not grow much further. Instead, we have as alternatives OS9 and Unixflex which are very solid operating systems environments. Functionally, these two are the class of the 8 bit world, and are an excellent transition tool into the 16 bit future. Meanwhile our two primary software vendors ISC and Microsoft will build on these operating systems and eventually build a bridge to the 68000. For those of us who want the best hardware and functionality of the 16 bit processor, there is no question at all where the real future is to be found - the Motorola 68000. Alreadr, the choice of machines based upon this chip and the operating systems which are available to support it are just simply incredible.

incredible selection of software and the access to the CP/M User's Group that I get with CP/M. Since CP/M is so widely used, the CP/M disk format recorded in single density on 5 1/4 drives has become another de facto industry standard for software distribution.

But if you can have only one of the two well, I guess you will just have to decide for yourself.

I've only touched on highlights if you have further insight or opinions I'd be most interested to hear them either in the pages of *Micro Journal*, or addressed to my above address. If sufficient interest is indicated, I am more than willing to narrow in on specific areas of concern in a future letter for or is this an article should Don Williams be interested in publishing this information for his readers.

Sincerely

So what kind? Which is the best? The answer, of course as it always is in this kind of comparison is that neither is universally better or worse. It depends on what is important to you and the use for which you intend to use the computer. It is almost always true that an end user who sees the computer only from the perspective of what she can learn from sitting at a terminal entering the computer's G/L routine can not tell what kind of computer is driving the application program. The business man hardly cares if it is a 286 or 6800, whether or not the bus is 8-30 or 3000, whether the architecture is written in Pascal or C. Does the machine successfully achieve the use for which it is intended in a cost effective fashion seems to be the important question.

From a hacker's point of view, I prefer the elegance of the 6800, the ease of use of Flex, ISC's documentation, and the reality that DEC and Unixflex hold for the future. I also like the collection of hardware boards, colour graphics boards, 720i, 8000i, 8010i, 8020i, 8030i, 8040i, 8050i, 8060i, 8070i, 8080i, 8090i, 8100i, 8110i, 8120i, 8130i, 8140i, 8150i, 8160i, 8170i, 8180i, 8190i, 8200i, 8210i, 8220i, 8230i, 8240i, 8250i, 8260i, 8270i, 8280i, 8290i, 8300i, 8310i, 8320i, 8330i, 8340i, 8350i, 8360i, 8370i, 8380i, 8390i, 8400i, 8410i, 8420i, 8430i, 8440i, 8450i, 8460i, 8470i, 8480i, 8490i, 8500i, 8510i, 8520i, 8530i, 8540i, 8550i, 8560i, 8570i, 8580i, 8590i, 8600i, 8610i, 8620i, 8630i, 8640i, 8650i, 8660i, 8670i, 8680i, 8690i, 8700i, 8710i, 8720i, 8730i, 8740i, 8750i, 8760i, 8770i, 8780i, 8790i, 8800i, 8810i, 8820i, 8830i, 8840i, 8850i, 8860i, 8870i, 8880i, 8890i, 8900i, 8910i, 8920i, 8930i, 8940i, 8950i, 8960i, 8970i, 8980i, 8990i, 9000i, 9010i, 9020i, 9030i, 9040i, 9050i, 9060i, 9070i, 9080i, 9090i, 9100i, 9110i, 9120i, 9130i, 9140i, 9150i, 9160i, 9170i, 9180i, 9190i, 9200i, 9210i, 9220i, 9230i, 9240i, 9250i, 9260i, 9270i, 9280i, 9290i, 9300i, 9310i, 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Conversion

You will need the following parts, along with a standard tool kit:

- two cylindrical brass or hard plastic shims 1" long, 1/8" ID, 3/8" OD,
- enough firm material 1/8" thick for two 3" diameter circles (may be cardboard, PC board, etc.),
- four small-headed 1" 4/40 bolts and washers and eight matching nuts.

Ensure that you have these parts before starting the conversion, or your printer may be down longer than you expected. Remove power, the loose plastic cover, and the ribbon from the printer. Move the head to the extreme right or left margin. If you feel it helpful, remove the top cover of the printer.

Place the shims over the ribbon reel shafts and LA-36 reels over the shims. They will not fit well because of the keys on the nylon gears, but hold the reels horizontal and mark the gear through the key holes on the reels. A mechanical pencil with lead over-extended may be used for this task. Remove the shims and reels.

Carefully remove the retaining rings from the shafts which support the ribbon reels, and work the washers, springs, and gears free from the shafts. All parts are symmetrically placed on the two shafts, so there is no right-left ambiguity of parts replacement. However, be sure not to lose any parts, as they may be difficult to replace or may cause mechanical failure if they land between two opposing moving parts.

Drill out the expanded hollow metal shafts holding the reel keys in the nylon gears. Try not to get the gear too hot, as it will melt. Pull the keys from the front. Using a bit the same size as the 4/40 bolts, drill the new key locations at the sites marked previously. Using a much larger drill, form countersink depressions in the back of the gears just deep enough so that the heads of the bolts will not protrude. Insert the bolts into the gears from the back, screw them completely into the gears, and put nuts on the top to secure them firmly to the gears. Ensure that the reels fit easily

onto the bolts, or make adjustments in the bolt positioning until they do fit.

Cut out two rings from the 1/8" thick material, with approximately 1 1/2" ID and 3" OD dimensions. Slip it over the fronts of the gears and trim if necessary to ensure that it lies flat on the gears and does not extend beyond the inside of the teeth on the outside of the gears. Attach with thin tape or glue to the gears.

Slip the gears back on the shafts, the shims back on the shafts, and the reels back on the shims. Ensure that the reels fit easily and do not strike the ribbon guides, the ribbon driver between the reels, and the head. If one strikes a ribbon guide, file the offending part of the guide until the reel does not strike it. If one strikes a ribbon driver, pad the 1/8" thick material evenly with vinyl tape until it clears. If one strikes the head (with the adjust lever in the number 2 position) bend the vertical ribbon mechanism evenly on both sides until both reels clear, keeping the top of the ribbon mechanism level.

Remove the shims and reels and replace the springs, washers, and retaining clips on the shafts. In case you did not observe the original order, the springs go first, followed by the large metal washers, the small mylar washers, and the clips. Replace the shims back on the shafts.

Re-install the ribbon on the LA-36 reels through all the guides used before except for running the ribbon on the opposite side of the ribbon guides adjacent to the reels. This is necessary because the LA-36 ribbon winds onto the reels at a large enough radius that the ribbon would otherwise miss the adjacent ribbon guides when the reel is nearly full. Install the 4/40 nuts on the bolts to secure the reels; if necessary, use the washers to prevent the bolts from slipping through the holes in the reels.

If the ribbon does not pull straight through the adjacent guides, pull up on the offending guide with a pair of pliers while rotating it slightly. If this does not correct it sufficiently,

file a little of the metal off the guide until it does fit. If you have done any filing here or earlier, be sure to collect all scrap metal filings.

At this point, the ribbon should run fairly easily from one reel to the other through all the guides, but not spin easily enough to leave slack at any point. If it does not, correct the problem.

When you are ready, put paper in the printer and run a self-test by holding the LINE FEED button while turning the power switch on. Be ready to turn power off quickly if the head snags the ribbon or you hear loud noises from the ribbon driver gears or from the head striking the reels. The reels should ride flat and smooth on the gears. Because the ribbon winds at a larger diameter on the new reels than on the old ones, the speed of the ribbon through the guides will be increased, and this will increase the strain on the ribbon and guides somewhat. If necessary, bend the

guides at the extreme margins slightly to ensure that the ribbon feeds smoothly and evenly without bunching.

Assuming all is OK, turn the power off, and remove the nuts securing the reels to the gears. Pull up on each reel. If the center shim comes up with the reel, remove the reel and shim. Wrap enough vinyl tape around the shaft (start with one turn) so that the shim does not easily turn on or come off the shaft; do not use glue, as that would prevent the removal of the nylon gears. Replace the reels on the shims and resecure them with the nuts (and washers, if used). Note the revised ribbon path on the inside of the loose plastic cover and replace all covers previously removed.

Summary

The preceding has described how to modify the OKIDATA 84 to use DEC LA-36 ribbons and reels. Such a modification has the advantage of making the replacement of ribbons simpler and more economical.

20 Box 6
Gray Court, SC 29643

April 13, 1983

The Editor
58 Micro Journal
5738 Cassandra Smith Road
Minneapolis, TN 37263

Sir:

We are pleased to announce the Metropolitan Greenville (SC) Color Computer Club formed in January of this year and already almost fifty members strong.

The COCC serves the interests of present and prospective CoCo owners in the entire Western South Carolina region. As a group, we are totally committed to computer literacy among ourselves and within the community. Members enjoy a lively exchange of computing information, free language, programming, and hardware tutorials as well as a bimonthly club newsletter.

Meetings are held every Tuesday night at 7:30 at the Plain Elementary School in Greenville, SC.

Anyone wanting more information about this dynamic organization may contact me any time at (803) 876-3328 or 3812, or write.

Ed Lowe
ED LOWE
Sec'y/Treas.



April 14, 1983

Dear Don:

We have wanted, for some time now, to use our "COCO" with a high quality monochrome monitor. We tried many of the published circuits, but the results were always disappointing. So, we designed our own.

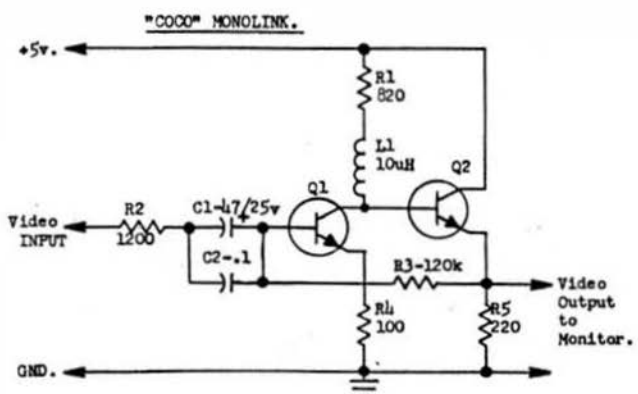
Our circuit has been tested on an 18MHz monitor in both the normal character and hi-res graphics modes. The quality, sharpness, and definition of display must be seen to be believed. Note that this circuit does not disturb the RF modulator system of the "COCO". In addition,

both your color television and a monochrome monitor may be used at the same time.

If we receive enough requests, we will offer the "MONOLINK" (TM) in kit form. You may publish this letter and information for the private use of "COCO" owners. We reserve the rights associated with this design for manufacture.

Thank you,

Ross Blank
ACS, Inc.



All resistors 1/4watt.
Q1-TC0108 Q2-TC0128 L1-J.W. Miller 70705A1
INPUTS to MONOLINK from "COCO" video mixer MCL372 chip.
+5 pin 11
Video pin 9
GND pin 4



11931 W BLUEMOUNT ROAD
SANTA FE, NM 87505



SIMPLIFIED PIA CONTROL REGISTER INFORMATION FOR USE WITH STANDARD HANDSHAKING

CTL LINE	CR BIT	0=CLEAR 1=SET
CA1 CB1	0	0=IRQ DISABLED, ALWAYS HIGH 1=IRQ ENABLED, GOES LOW ON SIGNAL
	1	0=HIGH TO LOW TRANSITION, ACTIVE 1=LOW TO HIGH TRANSITION, ACTIVE
DDR	2	0=ACCESS TO DATA DIRECTION REGISTER \$00=INPUT, \$FF=OUTPUT 1=ACCESS TO I/O PORT BUFFER
CA2 CB2	3	WRITE AN 0 TO SET CX2 LOW WRITE AN 1 TO SET CX2 HIGH
	4	1=STANDARD HANDSHAKING
	5	1=CX2 SET AS OUTPUT LINE
CA1 CB1	6	NOT USED, READ ONLY
	7	IRQ FLAG (READ ONLY)

ACORN COMPUTER SYSTEMS

April 23, 1983
7204 E. 28th St.
K.C., Mo. 64129

449 Micro Journal
5900 Cassandra Smith Rd.
Hixson, In. 37943

Dear Don Williams

Enclosed you'll find a check for a subscription to
449 Micro Journal. Just need to say a couple of things.
1) Does anyone know what ever happened to the \$6000
User's Group from Motorola. I tried to apply and my program
came back to me.
2) I'd like to make a special comment to one of your
advertisers, Midwest Scientific Instruments of Clatshe, Ka.
I had just ordered a couple of boards from the company
and was really surprised at the price of the boards. When
the box came in I was in shock to see the quality of the
boards. I have never seen a better built board. Needless
to say PSI will always have my business. The gentleman who
answered the phone not only sold me the boards but he also
was alot of help.

One more thing to comment on, is that it seems that
more and more manufactures are finally coming around to
the Motorola CPU's. I'm glad of that fact. So instead
having to write everything ourselves, we can now carry
buy some packages.

Best regards,
Adrianus C. Hoogstraad
Adrianus C. Hoogstraad

JIM SCHREIER

4327 East Grove Street
Phoenix, Arizona 85040

Dear Don,

Attempting to convert a Program to TSC's Basic recently proved
to be a real challenge. Working lightly may be the key to using
some of TSC Basic's features, but this patch of quicksand came as a
surprise. It seems to be in all editions and version of TSC's
Basics, including the 6809 release 22 of 12/04/81.

TSC's Basics divide in an inconsistent manner. Basic fails to
perform internal tests allowing the same displayed number to be
unequal. This happens for the number 1 as well as some other
numbers. It seems that the number one is stored differently in
memory, one number may be stored positive, the other, negative.
Nevertheless, they are displayed as equal, but test unequal.

Louis Boyd isolated the problem. These two routines
illustrate the difficulty and one possible solution.

'88' Micro Journal

```

5 REM Routine A
6 REM Incorrect Division Demo
10 DIGITS 17
20 FOR M=1 TO 20
30 M=1/X/X
40 M=1/X*X
50 PRINT M,M,
60 IF M<M THEN PRINT "NOT EQUAL";
70 PRINT
80 NEXT X

5 REM Routine B
6 REM Solution to Incorrect Division
10 DIGITS 17
20 FOR M=1 TO 20
30 M=1/X/X
40 M=1/X*X
50 PRINT M,M,
60 IF M<M THEN PRINT "NOT EQUAL";
70 PRINT
80 NEXT X

```

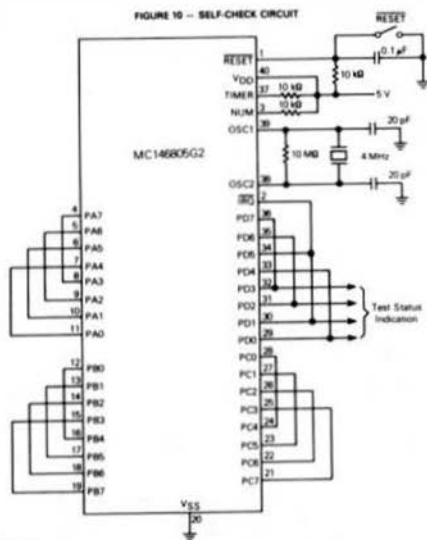
Since this appears to be an actual "bug", other TSC Basic
users may wish to contact me at (602) 276-6216 or Louis Boyd, 629
North 30th Street, Phoenix, Arizona 85008; (602) 275-8258; with
their comments and observations.

Best regards,
Jim Schreier

John P. Tucker
12015 O'Connor Road #184
San Antonio, Texas 78233

REVERSE, a game from Micro-Power; 1418 Thorndale;
Chicago, Illinois 60660. "(Giggle!) I beat the
computer -- again." You'll be hearing that often
when you play this delightful little Othello-like
game from Micro-Power. And you'll play it often
because of the good graphics it uses. My version
was for a SWTPCo 8212 terminal that I always run at
32,400 baud. Since Reverse runs in TSC's Extended
Basic, the screen changes as fast as information can
come out of the black box on my left. REVERSE
follows all of the rules of Othello, but the
mathematics used are set for it to play a relatively
low level of difficulty game. (I'm sending
Micro-Power my own version in hopes they'll
incorporate their clever presentation into a
stronger game.) However, each move is displayed in
large, easy-to-see "playing pieces" and the actual
changes and additions are displayed instead of the
screen being cleared and re-built each time. This
is a highly desirable feature and the authors are to
be congratulated on the manner in which this is
accomplished. Also enjoyable is the fact that
scoring is updated with each move. The program is
distributed in compiled form only, thus it was not
really practical to dig into it to make changes.
Perhaps Micro-Power would like to review this policy
on their games programs. I enjoyed the game and
commend it to your attention, keeping in mind that
it is more fun to play against a human opponent than
it is against the computer, in its present form.
The program allows you that choice. (The rules of
Othello are so widely known that they are not
mentioned here to conserve space. The documentation
is simple and occupies part of one side of one sheet
of paper, this including the explanation of the
features such as SKIP to skip a turn or TAKEBACK to
takeback a turn that shouldn't have been made, etc.
Yet, the documentation is adequate.)

In the event the external contractor needs to perform the self-check PPS, the interrupt pin (pin 2) should be connected to PD6 (pin 34) and not PD6 (pin 30). The connection was erroneously shown in the self-check cause diagram of the MC-100-2. Reference information: Data Sheet. The following diagram has been corrected to show the correct connection. See page 9 of the data sheet.



MOTOROLA INC. 1983

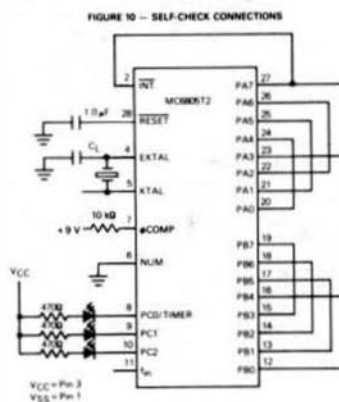
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A 10 k Ω resistor in series with B-VDD is required from pin 7 during pull test. A catastrophic breakdown occurs on pin 7. This resistor was omitted from the Self-Check Connector design found in the MC68010 Advanced Information Data Sheet. The following diagram has been corrected and replaces Figure 10 (page 8) of the data sheet.



ADAM-12

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The version of SUPER SLEUTH programs 2-80 B0805 object code on the 6800 1-9

These programs and macros enable the user to process 68001, 6805, 6802, 2-80, 8080/5 programs in original format. The TSC macro assembler is required for FLEX, UNIFLEX and the OSM assembler is required for OS-9.

These programs enable the user to interactively analyze, modify, and debug (14)6805 and 6502 object code.

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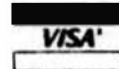
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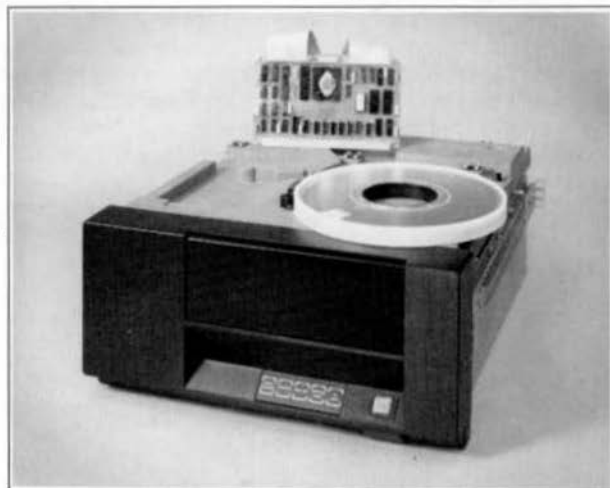


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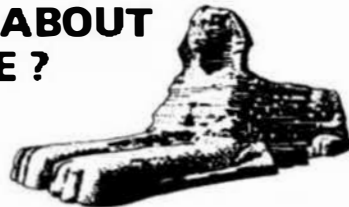
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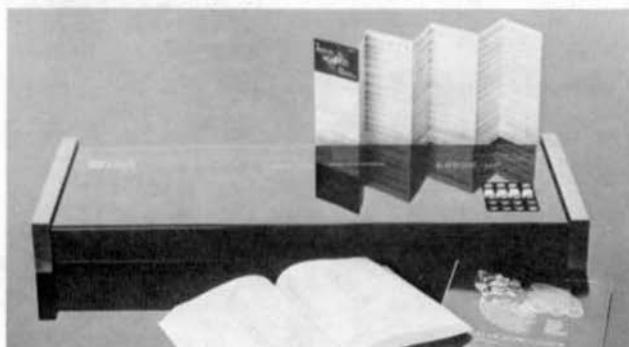
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2. Define account names, spacing, positioning, readings, and subaccounts
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Your Accounts Receivable can be followed with a minimum of time investment using these features:

1. Regular invoicing, debit and credit memos, full and partial payments
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1. Reports for quantities on hand, quantities on order, activity and many other categories
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5 B	•			•	•	•	
2708*							
2758	•	•	•	•	•	•	•
2516	•	•	•	•	•	•	•
2716	•	•	•	•	•	•	•
2716*							
2532	•	•	•	•	•	•	•
2732	•	•	•	•	•	•	•
27 2A	•	•	•	•	•	•	•
2564	•	•	•	•	•	•	•
2764	•	•	•	•	•	•	•
2528	•	•	•	•	•	•	•
27128	•						
2816			•				•
68764						•	
8748							
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TOTAL	11	3	12	6	11	11	11
PRICE	\$125	\$45*	\$169	\$289	\$375	\$489	\$575

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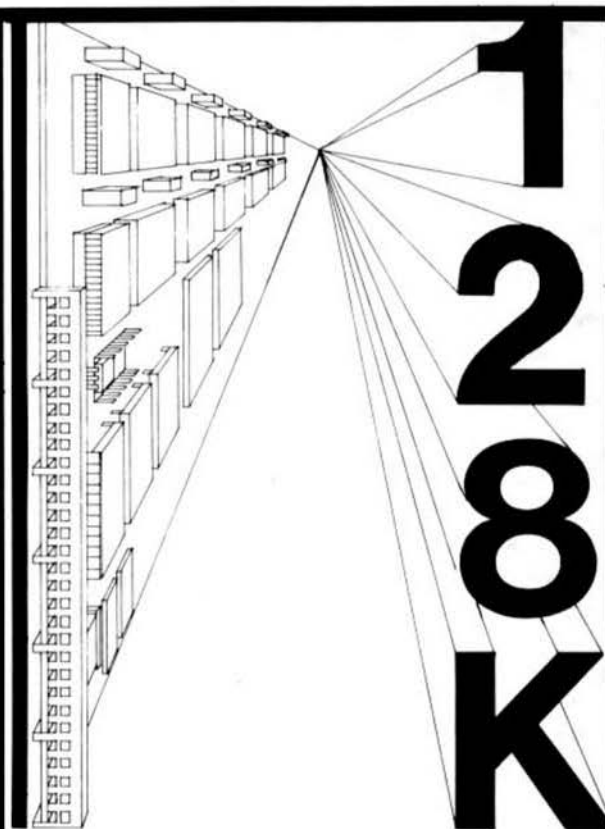
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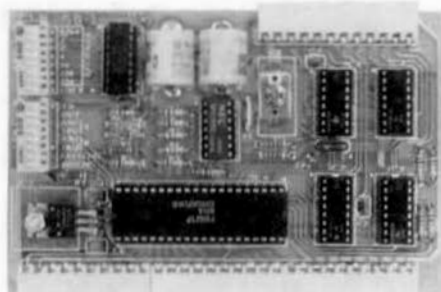
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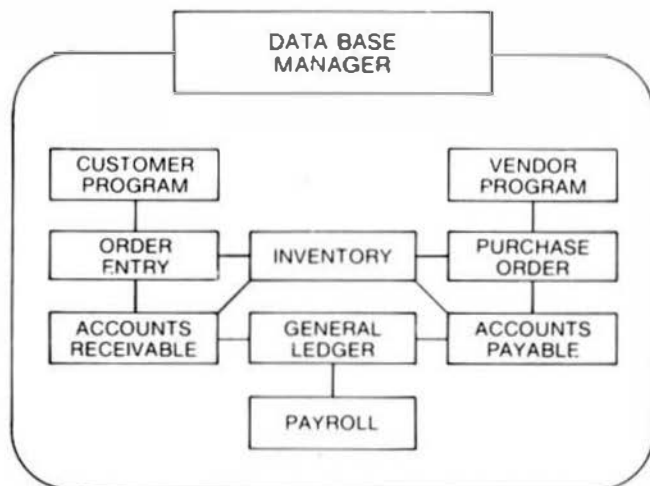
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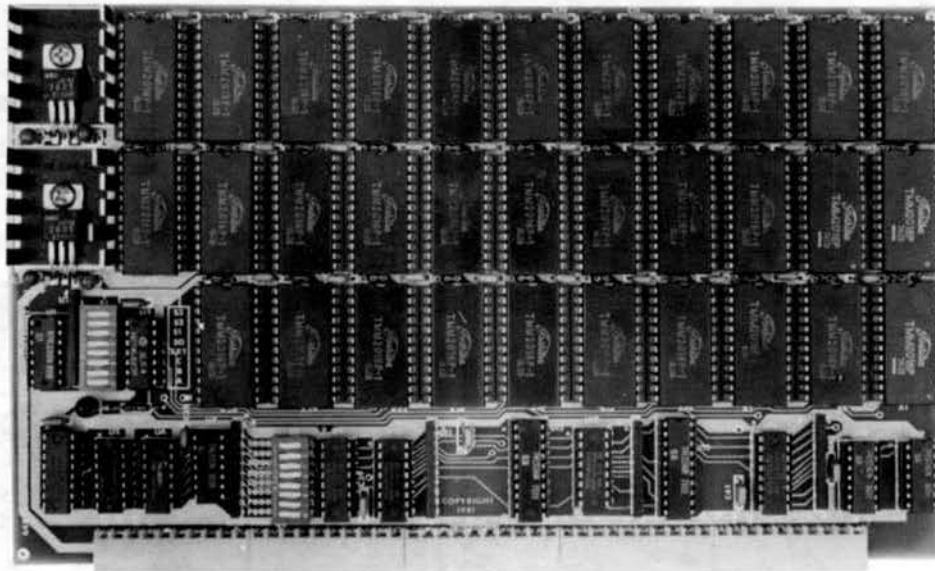
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DETAILED OVERVIEWS OF THE ABOVE PRODUCTS ARE ON PAGES 39/36 OF THE OCTOBER 1982 ISSUE OF '68 MICRO JOURNAL.

C

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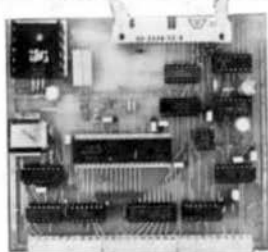
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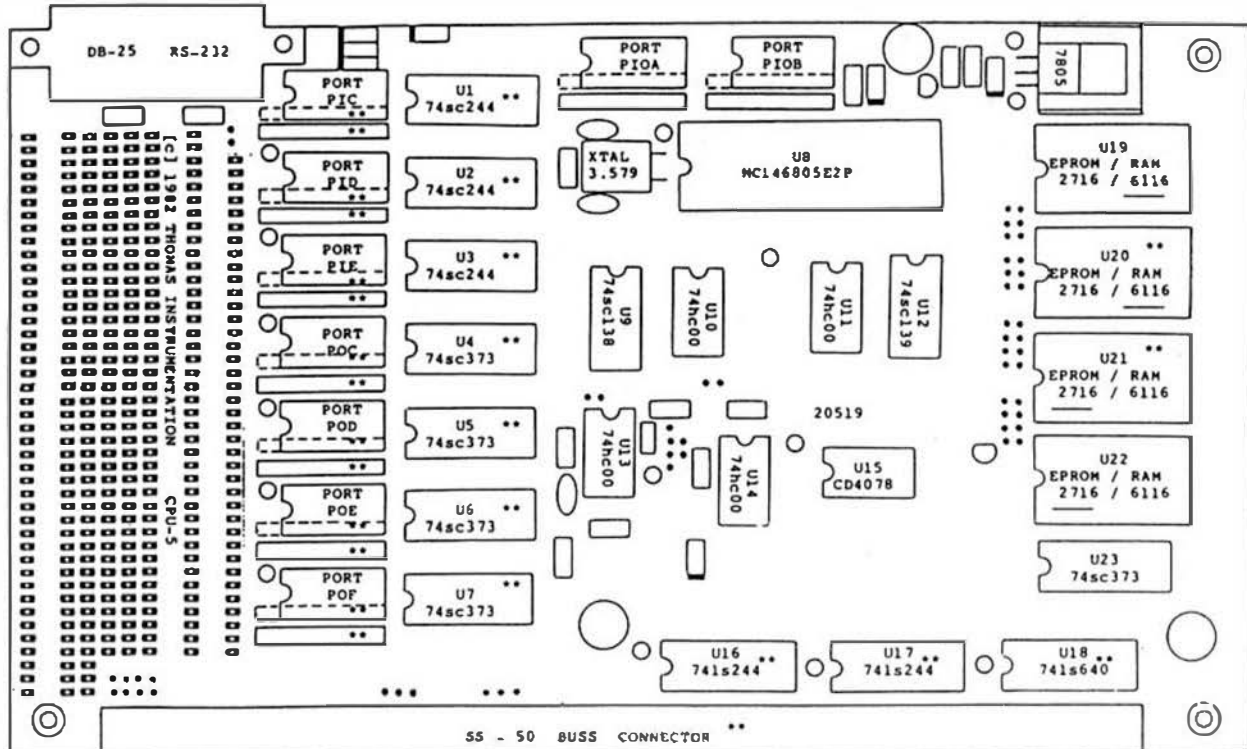
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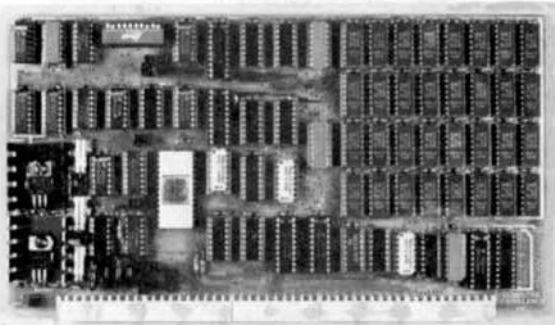
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ALL BOARDS AND SYSTEMS ARE ASSEMBLED, BURNED-IN, AND TESTED. GOLD-PLATED BUS CONNECTORS ARE USED.

TO ORDER BY MAIL: SEND CHECK OR MONEY ORDER OR USE YOUR VISA OR MASTER CHARGE. Please allow 3 weeks for personal checks to clear. U.S. orders add \$5 handling if order is under \$800.00. Foreign orders add \$10 handling if order is under \$200.00. Foreign orders over \$200.00 will be shipped via Emery Air Freight COLLECT, and we will charge no handling. All orders must be prepaid in U.S. funds. Please note that foreign checks have been taking about 8 weeks for collection so we would advise wiring money, or checks drawn on a bank account in the U.S. Our bank is the Continental Illinois National Bank of Chicago, 231 S. LaSalle Street, Chicago, IL 60603, account #73-32033. Visa or Master Charge also accepted.

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The GIMIX CLASSY CHASSIS #19 consists of a heavyweight aluminum cabinet, constant voltage ferro-resonant power supply, and \$550 Mother board with baud rate generator board.

Triple Disk regulator card and cables \$88.22 Baud rate generator card \$88.93

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#34 8K PROM board \$86.34

#32 16 socket PROM/ROM/RAM board \$238.32

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#43 2 port serial, RS232 \$128.43

#46 8 port serial, RS232 \$318.48

#42 2 port parallel \$86.42

#45 8 port parallel \$198.45

#50 serial, RS232, RS422, RS423 \$244.50

#52 SSDA serial, RS232, RS422, RS423 \$254.52

#54 ADLC serial, RS232, RS422, RS423 \$288.54

Each cable with connectors for back panel mounting (specify board) \$24.95

DISK CONTROLLERS

#68 DMA (featured in all systems above) \$588.68

#28 dbl. dens. programmed I/O (5" drives only) \$298.28

#58 single dens. programmed I/O (5" and/or 8" drives) \$276.58

#48 same as #58 but for 5" drives only \$198.48

Cable sets: 8" with Back Panel connector \$29.25

for two 8" external drives \$44.26

for two 5" drives \$34.96

SOFTWARE: GIMIX exclusive versions of OS-9/GIMIX I, II, III & FLEX are for GIMIX hardware only. All versions of OS-9 require the #68 controller.

When ordered with any controller, FLEX is \$38.00

GIMIXBUG PROMs and manual \$88.88

Boot or Video boot PROM \$30.00 UNIFLEX boot PROM \$50.00

OS-9 GIMIX I \$200.00 OS-9 GIMIX II \$300.00

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BASIC-09 \$200.00 RUNB \$100.00

DISK DRIVES FOR GIMIX SYSTEMS — complete with cables and power regulators.

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Cable for 4 drives \$87.84 Cable for cabinet to mainframe \$45.41

WINCHESTER SUBSYSTEMS: for use only in GIMIX systems with #68

DMA controller

#90: Includes one 19MB drive, interface, and Software \$3688.90

#91: Includes two 19MB drives, interface and Software \$5288.91

Contact GIMIX for price and availability of other forthcoming subsystems.

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#76 GHOST BOX24 VIDEO BOARD \$388.76

#66 50 pin Protoboard \$38.33

#33 30 pin Protoboard \$224.03

#06 6800 CPU \$288.08 Baud rate option, add \$30.00

#08 RELAY DRIVER (board, bracket, transformer, and 31 relays) \$1128.08

#86 - #08 (board, bracket, transformer, without relays) \$538.86

#85 OPTO board \$348.85

WINDRUSH EPROM PROGRAMMER \$375.00

3" Binder 12.00 2" Binder \$9.00

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If you are tired of playing games on your TRS-80C Color Computer, or find that you are handicapped by the limitations of the RS BASIC in trying to write a Program that will allow you to actually USE the Color Computer as a COMPUTER, YOU ARE READY TO MOVE UP TO THE FLEX9™ Operating System. If you want to have REAL PROGRAMMING POWER, using an Extremely Powerful Business BASIC, PASCALS, C Compilers, a full-blown Macro Assembler with a Library capability so you are not continuously reinventing the wheel, YOU ARE READY TO MOVE UP TO THE FLEX9™ Operating System. If you would like to see if YOU REALLY COULD USE A COMPUTER IN YOUR BUSINESS, or begin to make your Computer start PAYING ITS OWN WAY by doing some Computer Work for the millions of small businesses around you, such as Wordprocessing, Payroll, Accounting, Inventory, etc., then YOU ARE READY TO MOVE UP TO THE FLEX9™ Operating System. How?? DATA-COMP has the way!

DATA-COMP's FLEX9™ Conversion for the TRS-80C Color Computer was designed for the SERIOUS COMPUTER USER, with features like greatly increased Display Screens WITH Lower Case Letters, so you can put a FULL Menu on ONE Screen, or see SEVERAL Paragraphs at the same time, with features like providing a FULL Keyboard so you have FULL Control of your Computer AND 6 Programs NATURALLY, without needing a chart to see what Key Combination will give you what function, with USER ORIENTED functions to make using the Operating System natural, like having the Computer AUTOMATICALLY determine what type of Disk is being used in what type of Disk Drive and working accordingly, rather than you have to specify each and every thing for it, or like having the Computer work with the Printer you have been using all along without you having to tell the new Operating System what is there, etc.

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TERM — AN External Terminal Driver is FREE with purchase of F-Mate

SOFTWARE



Requires FLEX™ and one of the following CRT terminals

Now Runs On Any Type Terminal

Features:

- Two display boards.
- Four levels of play.
- Point scoring system.
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- Forfeit move.
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- Make move and swap sides.
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\$79.95 Specify 5" or 8" disk

This is one of the strongest CHESS programs running on any microcomputer, estimated USCF Rating 1600 -

Note: Personal checks allow 3-4 week delivery.

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DIET-TRAC Forecaster is a program that plans a diet in terms of either calories and percentage of carbohydrates, proteins and fats (C:P:F %) or grams of Carbohydrate, Protein and Fat food exchanges of each of the six basic food groups (vegetable, bread, meat, skim milk, fruit and fat) for a specific individual.

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FLEX VERSION \$59.95
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MX-70 \$355.00 MX-80 FT \$575.00



Color Computer External Terminal Program

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DATA-COMP has everything you need to make your TRS-80C Color Computer WORK for YOU, from Parts and Pieces to Full, Ready To Use SYSTEMS, DATA-COMP designs, sells, services, and SUPPORTS Computer SYSTEMS, not just Software. CALL DATA-COMP TODAY to make your Computer WORK FOR YOU!

System Requirements

FLEX9 Special General Version a Editor & Assembler (which normally sell for \$50.00 ea.)	\$150.00
F-MATE(RS) FLEX9 Conversion Rout. for the RS Disk Controller when purchased with Special General FLEX9 Sys.	\$49.95
when purchased without the General FLEX9 Sys.	\$59.95
Set of Eight 64K RAM Chips w/ Mod. Instructions	\$59.95
Color Computer with 64K RAM and EXT. BASIC	\$399.95

SPECIAL SYSTEM PACKAGES

64K Radio Shack COLOR COMPUTER, Radio Shack COLOR DISK CONTROLLER, a Disk Drive System, Special General Version of FLEX9™, F-MATE(RS)™ and a Box of 10 Double Density Diskettes, a COMPLETE, ready to run SYSTEM on your Color TV Set, \$1079.95

DISK DRIVE PACKAGES, etc.

These Packages include the Radio Shack Disk Controller, Disk Drives with Power Supply and Cabinet, and Disk Drive Cable.

PAK #1 — 1 Single Sided, Double Density Sys.	\$489.95
PAK #2 — 2 Single Sided, Double Density Sys.	\$749.95
PAK #3 — 1 Double Sided, Double Density Sys.	\$569.95
PAK #4 — 2 Double Sided, Double Density Sys.	\$919.95
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Radio Shack Disk Controller	\$179.95
1 Single Sided, Double Density Disk Drive Tandem	\$249.95
1 Double Sided, Double Density Disk Drive Curie	\$249.95
1 Curie Thirteen Double Sided, Double Density	\$279.95
Screen Clean — Clears Up Video Distortion On Your Color Computer	\$39.95
Single Drive Cabinet with Power Supply	\$89.95
Double Drive Cabinet with Power Supply	\$109.95
Single Drive Disk Cable for RS Controller	\$24.95
Double Drive Disk Cable for RS Controller	\$34.95
Micro Tech. Prods. Inc. LOWER CASE ROM Adapter	\$74.95
Radio Shack BASIC Version 1.1 ROM	\$34.95
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Single Side Single Density	\$2.75 ea.
Single Side Double Density	\$2.75 ea.
Double Side Double Density	\$4.92 ea.
Plastic Storage Box	\$2.00 ea.

8" Soft Sector Disks	
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Single Side Double Density	\$4.10 ea.
Double Side Double Density	\$4.75 ea.
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WINCHESTER BACKUP UTILITIES

The following utilities allow the backup of any size disk system to any size diskettes.

By simply inserting diskettes as requested by COPYMULT, a large disk system (Winchester, etc.) may be downloaded to your present floppy disk system, any size. No need to fiddle with directory deletions or any of the other tedious operations that must be done using a normal copy routine.

COPYMULT-CMD understands normal "copy" syntax and always keeps up with files already copied by maintaining directories for both host and receiving disk system, thus eliminating hours of tedious keyboard entries and other time consuming cleanup chores.

BACKUP-CMD is a special program that downloads "random" type files, any size.

RESTORE-CMD a special program to restructure copied "random" files for copying, or recopying back to the host system.

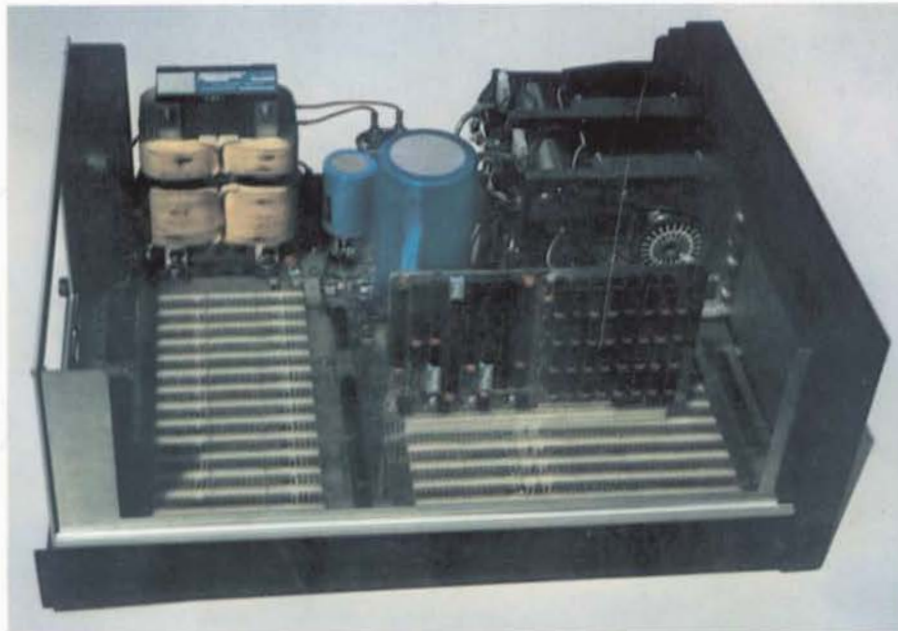
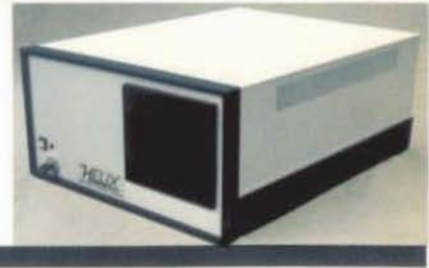
FREELINK-CMD a "bonus" utility that "relinks" the free chain of a floppy or hard disk thereby eliminating fragmentation.

** Completely documented source files Included.
** ALL 3 Programs \$99.50 on 8" diskette

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The HELIX™ computer system represents the latest advance in S-50 bus computer systems. Relying on the physical nature of S-50 bus connectors to guarantee compatibility, the HELIX adds 14 bus lines (becoming S-64) to allow a 68000 processor to operate with full 16 bit data transfer and 24 bit addressing, while at the same time providing full interchangeability with existing S-50 components.

Offered with a selection of processors, memories, and peripheral controllers, a HELIX system can be configured for applications ranging from advanced hobbyist to multiterminal time-sharing.

Designed to offer the utmost in speed, reliability, and utility at a reasonable price, it represents a new standard of quality for those who require a professionally designed computer for professional use.

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- DM-64
- Field Proven
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- Fully Transparent Refresh
- Tested at 2.5 MHz Operation
- DM-512
- 512K Bytes on a Single S-64 Board
- 16 Bit Power and 8 Bit Compatibility
- Runs in Existing S-50 Systems where Physical Space Allows
- Full 24 Bit Addressing
- Fully Transparent Refresh

THE PRICES

Because of the variety of configurations possible, full pricing cannot be given. Representative prices are:

- 64K 6809 HELIX..... \$2495
- 256K 6809 HELIX..... \$2895
- 512K 6809 HELIX..... \$3750
- 512K 68000 HELIX..... \$4195

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